

# Curriculum and Syllabus

**B. TECH.**

**Electronics and Communication Engineering**

(Applicable for 2015-16 batch and onwards)



**Department of Electronics and Communication Engineering  
School of Engineering and Technology,  
H. N. B. Garhwal (Central) University,  
Srinagar Garhwal, Uttarakhand- 246174**

## Curriculum

### Definitions/ Descriptions

#### 1. Credit Equivalent

	No. of Contact Hours per Week	Equivalent Credits
Lecture+ Tutorial	3 or 4	3
Practical	2	1

#### 2. Code for Courses:

Code for a course consists of two alphabets followed by three digits and an optional alphabet.

First three alphabet represent the school name (SET: School of Engineering and Technology) next two alphabets in the code represent the subject area of the course. E.g. (SH: Applied Science and Humanities, EC: Electronics and Communication Engineering, IN: Instrumentation Engineering, EE: Electrical Engineering, ME: Mechanical Engineering, CS: Computer Science and Engineering, IT: Information Technology, AECC: Ability Enhancement Compulsory Courses). Then there will be subject code with 4 letters out of which first will tell the nature of subject (C: Core/E: Elective/S: Skill Enhancement) and next three letters will tell the number according to the semester (for example 801 will tell its 8<sup>th</sup> semester subject). First digit represents the semester. Next two digits represent the sequence number of course in the list of courses of a semester.

#### Mandatory Qualifying Courses and Elective Course:

Syllabus contains Mandatory Qualifying Courses to familiarize students with certain study areas/ disciplines of importance. Students have to complete and qualify mandatory qualifying course. Marks obtained for these courses are not to be added for calculating total Marks.

Elective courses are provided in VII and VIII semesters to provide student with flexibility to choose courses of their interest from a list of offered electives. These Electives are the courses offered by the same department or other departments for the students.

## Semester-wise list of subjects

### Semester I

S. No.	Code	Course Title	L	T	P	Contact Hrs./Week	Credits
1	SET/SH/BT/C101	Mathematics I	3	1	-	4	3
2	SET/SH/BT/C102	Physics	3	1	-	4	3
	SET/SH/BT/C203	Chemistry					
3	SET/EE/BT/C103	Basic Electrical Engineering	3	1	-	4	3
	SET/ME/BT/C202	Basic Mechanical Engineering					
4	SET/EC/BT/C104	Basic Electronics	3	1	-	4	3
	SET/ME/BT/C204	Engineering Mechanics					
5	SET/IT/BT/C105	Fundamentals of Information Technology	3	1	-	4	3
	SET/CS/BT/C205	Computer Programming					
6	AECC106	*Environmental Science	2	-	-	2	2
7	SET/SH/BT/C106	Physics Lab	-	-	2	2	1
	SET/SH/BT/C207	Chemistry Lab					
8	SET/EE/BT/C107	Basic Electrical Engineering Lab	-	-	2	2	1
	SET/ME/BT/C206	Basic Mechanical Engineering Lab					
9	SET/IT/BT/C108	Information Technology Lab	-	-	2	2	1
	SET/CS/BT/C208	Computer Programming Lab					
10	SET/ME/BT/S109	**Engineering Graphics	-	-	4	4	2
<b>Total</b>			17	5	10	32	22

\* Ability Enhancement Compulsory course.

\*\*Skill Enhancement Course.

### Semester II

S. No.	Code	Course Title	L	T	P	Contact Hrs./Week	Credits
1	SET/SH/BT/C201	Mathematics II	3	1	-	4	3
2	SET/ME/BT/C202	Basic Mechanical Engineering	3	1	-	4	3
	SET/EE/BT/C103	Basic Electrical Engineering					
3	SET/SH/BT/C203	Chemistry	3	1	-	4	3
	SET/SH/BT/C102	Physics					
4	SET/ME/BT/C204	Engineering Mechanics	3	1	-	4	3
	SET/EC/BT/C104	Basic Electronics					
5	SET/CS/BT/C205	Computer Programming	3	1	-	4	3
	SET/IT/BT/C105	Fundamentals of Information Technology					
6	AECC206	*General English	2	-	-	2	2
7	SET/ME/BT/C206	Basic Mechanical Engineering Lab	-	-	2	2	1
	SET/EE/BT/C107	Basic Electrical Engineering Lab					
8	SET/SH/BT/C207	Chemistry Lab	-	-	2	2	1
	SET/SH/BT/C106	Physics Lab					
9	SET/CS/BT/C208	Computer Programming Lab	-	-	2	2	1
	SET/IT/BT/C108	Information Technology Lab					
10	SET/ME/BT/S209	**Engineering Workshop	-	-	4	4	2
<b>Total</b>			17	5	10	32	22

\* Ability Enhancement Compulsory course.

\*\*Skill Enhancement Course.

### Semester III

S. No.	Code	Course Title	L	T	P	Contact Hrs./Week	Credits
1	SET/SH/BT/C301	Mathematics III	3	1	-	4	3
2	SET/EC/BT/C302	Electronic Devices and Circuits	3	1	-	4	3
3	SET/EC/BT/C303	Digital Electronics	3	1	-	4	3
4	SET/IN/BT/C304	Signals and Systems	3	1	-	4	3
5	SET/IN/BT/C305	Circuit Theory	3	1	-	4	3
6	SET/EC/BT/C306	Electronic Devices and Circuits Lab	-	-	2	2	1
7	SET/EC/BT/C307	Digital Electronics Lab	-	-	2	2	1
8	SET/IN/BT/C308	Signals and Networks Lab	-	-	4	4	2
9	SET/EC/BT/S309	Electronics Servicing Lab*	-	-	4	4	2
<b>Total</b>			15	5	12	32	21

\*Skill Enhancement Course.

### Semester IV

S. No.	Code	Course Title	L	T	P	Contact Hrs./Week	Credits
1	SET/EC/BT/C401	Analog Integrated Circuits	3	1	-	4	3
2	SET/IN/BT/C402	Microprocessors and Interfacing	3	1	-	4	3
3	SET/EC/BT/C403	Analog Communication	3	1	-	4	3
4	SET/EC /BT/C404	Electromagnetic Field Theory	3	1	-	4	3
5	SET/IN/BT/C405	Electrical Measurements and Instrumentation	3	1	-	4	3
6	SET/EC/BT/C406	Analog Integrated Circuits Lab	-	-	2	2	1
7	SET/IN/BT/C407	Microprocessors and Interfacing Lab	-	-	2	2	1
8	SET/EC /BT/C408	Analog Communication Lab	-	-	2	2	1
9	SET/IN/BT/C409	Electrical Measurement and Instrumentation Lab	-	-	2	2	1
10	SET/EC /BT/S410	Mini Project I*	-	-	4	4	2
<b>Total</b>			15	5	12	32	21

\*Skill Enhancement Course.

### Semester V

S. No.	Code	Course Title	L	T	P	Contact Hrs./Week	Credits
1	SET/IN/BT/C501	Control Systems	3	1	-	4	3
2	SET/EC/BT/C502	Digital Communication	3	1	-	4	3
3	SET/IN/BT/C503	Electrical Machines	3	1	-	4	3
4	SET/EC/BT/C504	VLSI Technology	3	1	-	4	3
5	SET/EC/BT/C505	Antenna and Wave Propagation	3	1	-	4	3
6	SET/IN/BT/C506	Control Systems Lab	-	-	2	2	1
7	SET/EC/BT/C507	Digital Communication and Antenna Lab	-	-	2	2	1
8	SET/IN/BT/C508	Electrical Machines Lab	-	-	2	2	1
9	SET/EC/BT/C509	VLSI Technology Lab	-	-	2	2	1
10	SET/EC/BT/S510	Mini Project II*	-	-	4	4	2
<b>Total</b>			15	5	12	32	21

\*Skill Enhancement Course.

## Semester VI

S. No.	Code	Course Title	L	T	P	Contact Hrs./Week	Credits
1	SET/EC/BT/C601	Digital Signal Processing	3	1	-	4	3
2	SET/EC/BT/C602	Microwave Engineering	3	1	-	4	3
3	SET/EC/BT/C603	Microcontrollers and Applications	3	1	-	4	3
4	SET/EC/BT/C604	Power Electronics	3	1	-	4	3
5	SET/EC/BT/C605	Computer System Architecture	3	1	-	4	3
6	SET/EC/BT/C606	Digital Signal Processing Lab	-	-	2	2	1
7	SET/EC/BT/C607	Microwave Engineering Lab	-	-	2	2	1
8	SET/EC/BT/C608	Microcontrollers and Applications Lab	-	-	2	2	1
9	SET/EC/BT/C609	Seminar	-	-	2	2	1
10	SET/EC/BT/S610	Mini Project III*	-	-	4	4	2
<b>Total</b>			15	5	12	32	21

\*Skill Enhancement Course.

## Semester VII

S. No.	Code	Course Title	L	T	P	Contact Hrs./Week	Credits
1	SET/SH/BT/C701	Principles of Management	3	1	-	4	3
2	SET/EC/BT/C702	VLSI Deign	3	1	-	4	3
3	SET/EC/BT/C703	Optical Fiber Communication	3	1	-	4	3
4		Elective I	3	1	-	4	3
5		Elective II	3	1	-	4	3
6	SET/EC/BT/C710	VLSI Deign Lab	-	-	2	2	1
7	SET/EC/BT/C711	Optical Fiber Communication Lab	-	-	2	2	1
8	SET/EC/BT/C712	Project Preparation	-	-	4	4	2
9	SET/EC/BT/S713	Industrial Training Seminar*	-	-	4	4	2
<b>Total</b>			15	5	12	32	21

\*Skill Enhancement Course.

Elective I	S. No.	Code	Course Title
	1	SET/EC/BT/E704	Advance Semiconductor Devices
	2	SET/EC/BT/E705	Telecommunication Switching
	3	SET/EC/BT/E706	Digital Image Processing

Elective II	S. No.	Code	Course Title
	1	SET/EC/BT/E707	Data Communication and Networking
	2	SET/EC/BT/E708	FPGA Based Digital Design
	3	SET/EC/BT/E709	Information Theory and Coding

## Semester VIII

S. No.	Code	Course Title	L	T	P	Contact Hrs./Week	Credits
1	SET/EC/BT/C801	Wireless and Mobile Communication	3	1	-	4	3
2	SET/EC/BT/C802	Embedded Systems	3	1	-	4	3
3		Elective III	3	1	-	4	3
4		Elective IV	3	1	-	4	3
5	SET/IN/BT/C809	Advanced Communication Lab	-	-	2	2	1
6	SET/IN/BT/C810	CAD Lab	-	-	2	2	1
7	SET/IN/BT/C811	Major Project	-	-	14	14	7
<b>Total</b>			12	4	18	34	21

Elective III	S. No.	Code	Course Title
	1	SET/EC/BT/E803	Radar Guidance And Navigation
	2	SET/EC/BT/E804	IC Fabrication And Testing
	3	SET/IN/BT/E805	Renewable Energy Engineering

Elective IV	S. No.	Code	Course Title
	1	SET/EC/BT/E806	Satellite Communication
	2	SET/EC/BT/E807	Multimedia Systems And Communication
	3	SET/EC/BT/E808	CMOS Analog IC Design

### Note

- (1) Topic for the Seminar in 6<sup>th</sup> semesters shall be chosen by students in consultation with faculty. Topic shall not be mentioned in the syllabus anywhere, however, it should be related to Instrumentation Engineering.
- (2) In 4<sup>th</sup>, 5<sup>th</sup> and 6<sup>th</sup> semester, Mini Project work can be carried out individually or by a group of maximum of five students under the guidance of faculty. A committee of examiners will evaluate the projects.
- (3) Students shall choose 2 elective subjects in 7<sup>th</sup> and 8<sup>th</sup> semester each from the given Table. An elective subject shall be offered only when at least 30% of the intake opts for that subject. Major Project work shall be carried out during the 7<sup>th</sup> and 8<sup>th</sup> semester. Students can undertake Major Project individually or in group of not more than five students, under the guidance of a faculty or a group of faculty. Students have to present Synopsis of Major Project during the 7<sup>th</sup> semester. Feasibility of the Project shall be assessed by the project evaluation committee of the department before the end of 7<sup>th</sup> semester. However, Major Project would be evaluated in the end of 8<sup>th</sup> semester.

## Detailed Syllabi

### SEMESTER I

S. No.	Code	Course Title	L	T	P	T.A	C.T	TOT	ESE	SUB. TOTAL	Credits
1	SET/SH/BT/C101	Mathematics I	3	1	-	10	20	30	70	100	3
2	SET/SH/BT/C102	Physics	3	1	-	10	20	30	70	100	3
	SET/SH/BT/C203	Chemistry									
3	SET/EE/BT/C103	Basic Electrical Engineering	3	1	-	10	20	30	70	100	3
	SET/ME/BT/C202	Basic Mechanical Engineering									
4	SET/EC/BT/C104	Basic Electronics	3	1	-	10	20	30	70	100	3
	SET/ME/BT/C204	Engineering Mechanics									
5	SET/IT/BT/C105	Fundamentals of Information Technology	3	1	-	10	20	30	70	100	3
	SET/CS/BT/C205	Computer Programming									
6	AECC106	*Environmental Science	2	-	-	10	20	30	70	100	2
7	SET/SH/BT/C106	Physics Lab	-	-	2	30	-	30	70	100	1
	SET/SH/BT/C207	Chemistry Lab									
8	SET/EE/BT/C107	Basic Electrical Engineering Lab	-	-	2	30	-	30	70	100	1
	SET/ME/BT/C206	Basic Mechanical Engineering Lab									
9	SET/IT/BT/C108	Information Technology Lab	-	-	2	30	-	30	70	100	1
	SET/CS/BT/C208	Computer Programming Lab									
10	SET/ME/BT/S109	**Engineering Graphics			4	30	-	30	70	100	2
<b>Total</b>			16	5	10	180	120	300	700	1000	22

\* Ability Enhancement Compulsory course.

\*\*Skill Enhancement Course.

L - Lecture hours, T - Tutorialhours, P - Practicalhours, T.A - Teacher's Assessment, C.T - Class Test, TOT - Total, ESE - End Semester Examination.

<b>SET/SH/BT/C101. MATHEMATICS I</b>		
Module Name	Content	No. of Hrs.
<b>Vector Calculus</b>	Interpretation of Vectors & Scalars, Gradient, Divergence and Curl of a Vector and Their Physical Interpretation, Gauss Divergence Theorem and Stoke's Theorem.	8
<b>Matrices</b>	Elementary Row and Column Transformation, Linear Dependence, Rank of Matrix, Consistency of System of Linear Equation and Solution of Linear System of Equations. Characteristic Equation, Cayley-Hamilton Theorem, Eigen Values and Eigen Vectors, Diagonalization, Complex Matrices.	13
<b>Differential Calculus</b>	Libnitz theorem, Partial Differentiation, Euler's Theorem, Asymptotes, Curve Tracing, Envelops and Evolutes. Change of Variables, Jacobians, Expansion of Functions of One and Several Variables. Cylindrical and Spherical Coordinate System. Approximation of Errors. Extrema of Function of Several Variables, Lagrange's Method.	13
<b>Probability and Statistics</b>	Binomial Distribution, Normal Distribution and Poisson's Distribution. Correlation and Regression.	8
<b>Total No. of Hours</b>		<b>42</b>
<b>Textbooks</b>	1. B. S. Grewal, Higher Engineering Mathematics, Khanna Publishers. 2. H K Das, Advanced Engineering Mathematics, S Chand. 3. Erwin Kreyszig, Advanced Engineering Mathematics.	
<b>References</b>	1. Shanti Narayan, A Text Book of Matrices, S. Chand . 2. Finney Thomas, Calculus and Analytical Geometry, Narosa Publication House. 3. N. Piskunov, Differential and Integral Calculus.	

SET/SH/BT/C102. PHYSICS		
Module Name	Content	No. of Hrs.
<b>Optics</b>	Interference: Coherent Sources, Conditions of Interference, Fresnel's Biprism Experiment, Interference in Thin Films, Newton's Rings; Single and n-Slit Diffraction, Diffraction Grating, Rayleigh's Criterion of Resolution, Resolving Power of Telescope, microscope; Phenomenon of Double Refraction, Ordinary and Extra-ordinary Rays, Nicol Prism, Circularly and Elliptically Polarized Light, Fresnel Theory, Optical Activity, Specific Rotation.	13
<b>Lasers and X-Rays</b>	Laser: Principle of Laser Action, Einstein's Coefficients, Construction and Working of He-Ne and Ruby Laser; Introduction to Maser. Diffraction of X-Rays, Bragg's Law, Practical Applications of X-Rays, Compton Effect.	7
<b>Basics Material Science</b>	Introduction to crystal structure of materials, Miller indices for crystallographic planes and directions. X-ray diffraction for determination of crystal structure. Defects in solids: point, line and planar defects and their effect on properties of materials. Band theory of solids, conductors, semi-conductors and insulators, metals. Fermi Level. Magnetism: dipole moments, paramagnetism, Curie's law, magnetization and hysteresis, Ferromagnetism and Anti-Ferromagnetism. Ferroelectricity and Piezoelectricity. Superconductivity in materials.	14
<b>Electromagnetics</b>	Ampere's Law and Displacement Current, Maxwell's Equations in Integral and Differential Forms, Electromagnetic Wave Propagation in Free Space and Conducting Media, Poynting Theorem.	8
<b>Total No. of Hours</b>		<b>42</b>
<b>Textbooks</b>	<ol style="list-style-type: none"> <li>Gaur, Gupta, Engineering Physics</li> <li>Callister W.D., Materials Science and Engineering: An introduction, 6th Edition, John Wiley &amp; Sons Inc., New York 2002</li> </ol>	
<b>References</b>	<ol style="list-style-type: none"> <li>J. R. Taylor, C.D. Zafiratos and M. A. Dubson, Modern Physics for Scientists and Engineers, 2nd Ed., Pearson (2007)</li> <li>Arthur Beiser, Concepts of Modern Physics, 6th Ed., TMH, (2009)</li> <li>A.K. Ghatak : Optics</li> <li>Subramanyam, Brijlal : Optics</li> <li>WehrRichards&amp;Adiav : Physics of Atoms</li> <li>O.Svelto : Lasers</li> <li>D.J. Griffith : Electrodynamics</li> <li>Robert Eisberg and Robert Resnick, Quantum Physics of atoms, Molecules, Solids, Nuclei and Particle, 2nd Ed., John Wiley(2006)</li> <li>Raghavan V. Materials Science and Engineering A first course 5th Edition, Prentice Hall, New Delhi, 1998</li> <li>Van Vlack, LH, Elements of Materials Science and Engineering. 6th Edition, Addison Wesley Singapore, 1989</li> <li>B. G. Streetman, Solid state Devices, 5th Ed., Pearson (2006)</li> <li>Dekker, Electrical Engineering Materials, PHI</li> </ol>	



SET/EE/BT/C103. BASIC ELECTRICAL ENGINEERING		
Module Name	Content	No. of Hrs.
<b>DC Networks</b>	Concepts of linear, nonlinear, active, passive, unilateral and bilateral elements; Ideal and practical voltage & current sources & conversion from one from the other; Kirchhoff's laws & statements; Mesh Analysis; Nodal Analysis; Delta-Star & Star-Delta conversion; Superposition principle; Thevenin's theorem & statement, advantages in case of complex networks; explanation & illustration with examples; Norton's theorem, Maximum power transfer theorem, Reciprocity Theorem and its application.	10
<b>Single Phase AC Circuits</b>	Generation of single phase a.c. voltage and determination of average (mean) and RMS (effective) values of voltage and current with special reference to sinusoidal waveforms; Form factor and peak factor for various waves; Representation of sinusoidal time varying quantities as phasors; concepts of reactance, impedance and their representation in complex forms using j operator; Steady state analysis of series R-L-C circuit & its phasor diagram; Concept of power & power factor; Concept of admittance, susceptance in parallel circuits; Analysis of series parallel circuits & phasor diagrams; Resonance in series and parallel circuits.	10
<b>Three Phase Circuits</b>	Generation of 3-phase balanced sinusoidal voltage; star & delta connections; line & phase quantities (current & voltage); Solution of 3-phase star/delta circuits with balanced supply voltage and balanced load; phasor diagram; 3-phase, 4-wire circuits; Measurement of three phase power by two wattmeter method; phasor diagram with balanced load and determination of load power factor from wattmeter readings.	6
<b>Transformers and Rotating Machines</b>	Transformers: Constructional features and principle of operation, concept of ideal transformer under no load & loaded conditions and its equivalent circuit; Practical transformer rating & its equivalent circuit; Autotransformer & principle of operation & relative advantages & disadvantages; Rotating Machine: construction features (stator, rotor & air gap), conditions for production of steady electromagnetic torque; Three phase Induction motor: constructional features and operation; DC Machines: construction features, EMF and Torque expression, Classification of D.C. motors and generators; Stepper motor.	12
<b>Measuring Instruments</b>	DC PMMC instruments & constructional feature and principle of operation; Moving iron meters & construction and principle of operation; Dynamometer type wattmeter; Induction type energy meter construction & principle of operation.	6
<b>Total No. of Hours</b>		<b>44</b>
<b>Textbooks</b>	1. I.J. Nagrath, "Basic Electrical Engineering," Tata Mc. Graw Hill.	
<b>References</b>	1. A. E. Fitzgerald, D.E., Higginbotham and A.Grabel, "Basic Electrical Engineering," McGraw Hill. 2. Rizzoni, Principles and Applications of Electrical Engineering, TMH. 3. V. Del Toro. "Principles of electrical Engineering," Prentice hall. 4. W.H. Hayt & J.E. Kemmerly, "Engineering circuit Analysis," McGraw Hill. 5. H. Cotton, "Advanced Electrical Technology" Wheeler Publishing.	

<b>SET/EC/BT/C104. BASIC ELECTRONICS</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Semiconductor Diodes</b>	Semiconductor materials- intrinsic and extrinsic types, Ideal Diode as switch, Terminal characteristics of PN diode - p-n junction under open circuit condition, p-n junction under forward bias and reverse bias conditions, p-n junction in breakdown region; Zener diode and applications e.g. voltage regulator; Rectifier Circuits, Clipping and Clamping circuits; LED, Photo Diode.	10
<b>Bipolar Junction Transistors</b>	Physical structure, physical operation and current-voltage characteristics of NPN transistor; Use of Voltage dependent Current source as an Voltage amplifier; Transistor as an amplifier: Characteristics of CE amplifier; Active region operation of transistor; D.C. analysis of Common Emitter Amplifier: load line analysis; Transistor as a switch: cut-off and saturation modes.	10
<b>Field Effect Transistor</b>	Enhancement-type MOSFET: structure and physical operation, current-voltage characteristics; MOSFET as a Switch, MOSFET as a Voltage dependent Current source and Amplifier.	8
<b>Operation Amplifier</b>	Ideal Op-amp; Properties of the ideal Operational Amplifier; op-amp application circuits (assuming ideal op amp): inverting amplifier, non -inverting amplifier, weighted summer, integrator, and differentiator.	6
<b>Digital Logic and Gates</b>	Binary, octal and hexadecimal number systems; Methods of base conversions; Binary, octal and hexadecimal arithmetic; Representation of signed numbers; Basic logic operations and logic gates; MOSFET Switch Implementation of Logic Gates e.g. Inverter, NAND, NOR. Basic postulates and fundamental theorems of Boolean algebra.	8
<b>Total No. of Hours</b>		<b>42</b>
<b>Textbooks</b>	1. Agarwal, Anant; Lang, Jeffrey H, δFoundations of Analog and Digital Electronic Circuitsö, Elsevier Science & Technology Books.	
<b>References</b>	<ol style="list-style-type: none"> <li>1. V. Del Toro, Principles of Electrical Engineering, PHI.</li> <li>2. Rizzoni, Principles and Applications of Electrical Engineering, TMH.</li> <li>3. Malvino, Electronic Principles.</li> <li>4. R.L.Boylestad&amp;L.Nashelsky, Electronics Devices &amp; Circuit Theory, PHI.</li> </ol>	

<b>SET/IT/BT/C105. FUNDAMENTALS OF INFORMATION TECHNOLOGY</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Introduction</b>	Definition of Electronic Computer, History, Generations, Characteristic and Application of Computers, Classification of Computers, Computer Hardware and Basic Computer Organization: CPU- ALU, CU; RAM/ROM, Various I/O devices, Peripherals, Storage Media.	4
<b>Computer Languages</b>	Binary, Hexadecimal Number System; Basic Binary Logic Operations; Binary Addition and Subtraction; Generation of Languages, Assembly Language, High level language; Translators, Interpreters, Compilers, Compilers; Flow Charts, Dataflow Diagram, Pseudo codes; Assemblers, Introduction to 4GLs.	6
<b>OS &amp; Office</b>	Software- System and Application Software; Elementary Concepts in Operating System; Textual Vs GUI Interface, Introduction to DOS, MS Windows.	4
<b>Computer Networks</b>	Elements of Communication system; Brief Introduction to Computer Networks- Introduction of LAN and WAN. Network Topologies, Client-server Architecture.	6
<b>Internet</b>	Internet & World Wide Web, Hypertext Markup Language, DHTML, WWW, Gopher, FTP, Telnet, Web Browsers, Net Surfing, Search Engines, Email; Introduction to Web Development, Static and Dynamic Pages.	6
<b>IT Application and Multi media</b>	Basic Awareness of NICNET and ERNET; E Commerce, E governance; Brief Introduction to Different Formats of Image, Audio, Video.	6
<b>Information Concepts &amp; Processing</b>	Definitions of Information , Need of information, quality of information, value of information, concept of information, Entropy category and Level of information in Business Organization, Data Concepts and Data Processing, Data Representation, Application of IT to E-commerce, Electronic Governance, Multimedia, Entertainment, Introduction to Information System.	8
<b>Total No. of Hours</b>		<b>40</b>
<b>Textbooks</b>	1. Sinha, Sinha, "Computer Fundamentals". 2. Yadav R. P., "Information Technology".	
<b>References</b>	1. D S Yadav, "Foundations of IT", New Age, Delhi. 2. Rajaraman, "Introduction to Computers", PHI. 3. Peter Nortans "Introduction to Computers", TMH. 4. Patterson D.A. & Hennessy J.L., "Computer Organization and Design", Morgan Kaufmann Publishers.	

<b>AECC106. ENVIRONMENT SCIENCE</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Introduction to Environmental Sciences</b>	Multidisciplinary nature of Environmental Sciences; Scope and importance; Concept of sustainability and sustainable development.	2
<b>Ecosystems</b>	What is an ecosystem? Structure and function of ecosystem; Energy flow in an ecosystem: food chains, food webs and ecological succession. Case studies of the following ecosystems : a. Forest ecosystem b. Grassland ecosystem c. Desert ecosystem d. Aquatic ecosystems (ponds, streams, lakes, rivers, oceans, estuaries)	6
<b>Natural Resources: Renewable and Non-renewable Resources</b>	Land resources and land use change; Land degradation, soil erosion and desertification. Deforestation: Causes and impacts due to mining, dam building on environment, forests, biodiversity and tribal populations. Water: Use and over-exploitation of surface and ground water, floods, droughts, conflicts over water (international & inter-state). Energy resources: Renewable and non renewable energy sources, use of alternate energy sources, growing energy needs, case studies.	8
<b>Biodiversity and Conservation</b>	Levels of biological diversity : genetic, species and ecosystem diversity; Biogeographic zones of India; Biodiversity patterns and global biodiversity hot spots India as a mega-biodiversity nation; Endangered and endemic species of India Threats to biodiversity: Habitat loss, poaching of wildlife, man-wildlife conflicts, biological invasions; Conservation of biodiversity: In-situ and Ex-situ conservation of biodiversity. Ecosystem and biodiversity services: Ecological, economic, social, ethical, aesthetic and Informational value.	8
<b>Environmental Pollution</b>	Environmental pollution : types, causes, effects and controls; Air, water, soil and noise pollution Nuclear hazards and human health risks Solid waste management: Control measures of urban and industrial waste. Pollution case studies.	8
<b>Environmental Policies &amp; Practices</b>	Climate change, global warming, ozone layer depletion, acid rain and impacts on human communities and agriculture Environment Laws: Environment Protection Act 1986; Air (Prevention & Control of Pollution) Act 1981; Water (Prevention and control of Pollution) Act 1974; Wildlife Protection Act 1972; Forest Conservation Act 1980. International agreements: Montreal protocol, Kyoto protocol and Convention on Biological Diversity (CBD). Nature reserves, tribal populations and rights, and human wildlife conflicts in Indian context.	7
<b>Human Communities and the Environment</b>	Human population growth: Impacts on environment, human health and welfare. Resettlement and rehabilitation of project affected persons; case studies. Disaster management: floods, earthquake, cyclones and landslides. Environmental movements: Chipko, Silent valley, Bishnois of Rajasthan. Environmental ethics: Role of Indian and other religions and cultures in environmental conservation. Environmental communication and public awareness, case studies (e.g., CNG vehicles in Delhi).	6

<b>Field work</b>	Visit to an area to document environmental assets: river/ forest/ flora/fauna, etc. Visit to a local polluted site-Urban/Rural/Industrial/Agricultural. Study of common plants, insects, birds and basic principles of identification. Study of simple ecosystems-pond, river, lake, forest patch, grassland, Delhi Ridge, etc.	5
<b>Total No. of Hours</b>		<b>50</b>
<p><b>Suggested Readings:</b></p> <ol style="list-style-type: none"> <li>1. Carson, R. 2002. <i>Silent Spring</i>. Houghton Mifflin Harcourt.</li> <li>2. Gadgil, M., &amp; Guha, R. 1993. <i>This Fissured Land: An Ecological History of India</i>. Univ. of California Press.</li> <li>3. Gleeson, B. and Low, N. (eds.) 1999. <i>Global Ethics and Environment</i>, London, Routledge.</li> <li>4. Gleick, P. H. 1993. <i>Water in Crisis</i>. Pacific Institute for Studies in Dev., Environment &amp; Security. Stockholm Env. Institute, Oxford Univ. Press.</li> <li>5. Groom, Martha J., Gary K. Meffe, and Carl Ronald Carroll. <i>Principles of Conservation Biology</i>. Sunderland: Sinauer Associates, 2006.</li> <li>6. Grumbine, R. Edward, and Pandit, M.K. 2013. Threats from India's Himalaya dams. <i>Science</i>, 339: 36-37.</li> <li>7. McCully, P. 1996. <i>Rivers no more: the environmental effects of dams</i> (pp. 29-64). Zed Books.</li> <li>8. McNeill, John R. 2000. <i>Something New Under the Sun: An Environmental History of the Twentieth Century</i>.</li> <li>9. Odum, E.P., Odum, H.T. &amp; Andrews, J. 1971. <i>Fundamentals of Ecology</i>. Philadelphia: Saunders.</li> <li>10. Pepper, I.L., Gerba, C.P. &amp; Brusseau, M.L. 2011. <i>Environmental and Pollution Science</i>. Academic Press.</li> <li>11. Rao, M.N. &amp; Datta, A.K. 1987. <i>Waste Water Treatment</i>. Oxford and IBH Publishing Co. Pvt. Ltd.</li> <li>12. Raven, P.H., Hassenzahl, D.M. &amp; Berg, L.R. 2012. <i>Environment</i>. 8th edition. John Wiley &amp; Sons.</li> <li>13. Rosencranz, A., Divan, S., &amp; Noble, M. L. 2001. <i>Environmental law and policy in India</i>. Tripathi 1992.</li> <li>14. Sengupta, R. 2003. <i>Ecology and economics: An approach to sustainable development</i>. OUP.</li> <li>15. Singh, J.S., Singh, S.P. and Gupta, S.R. 2014. <i>Ecology, Environmental Science and Conservation</i>. S. Chand Publishing, New Delhi.</li> <li>16. Sodhi, N.S., Gibson, L. &amp; Raven, P.H. (eds). 2013. <i>Conservation Biology: Voices from the Tropics</i>. John Wiley &amp; Sons.</li> <li>17. Thapar, V. 1998. <i>Land of the Tiger: A Natural History of the Indian Subcontinent</i>.</li> <li>18. Warren, C. E. 1971. <i>Biology and Water Pollution Control</i>. WB Saunders.</li> <li>19. Wilson, E. O. 2006. <i>The Creation: An appeal to save life on earth</i>. New York: Norton.</li> <li>20. World Commission on Environment and Development. 1987. <i>Our Common Future</i>. Oxford University press</li> </ol>		

<b>SET/SH/BT/C106. PHYSICS LAB</b>		
<b>Module</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Module 1</b>	<ol style="list-style-type: none"> <li>To determine the wavelength of monochromatic light by Newton's ring method.</li> <li>To determine the wavelength of monochromatic light by Fresnel's biprism.</li> <li>To determine the focal length of two lenses by nodal slide and locate the position of cardinal points.</li> <li>To determine the wavelength of spectral lines using plane transmission grating.</li> </ol>	6x2
<b>Module 2</b>	<ol style="list-style-type: none"> <li>Measurement of Magnetic susceptibility- Quincke's Method / Gouy's balance.</li> <li>Mapping of magnetic field.</li> </ol>	2x2
<b>Module 3</b>	<ol style="list-style-type: none"> <li>Measurement of e/m of electron's Thomson's experiment.</li> <li>Determination of Planck's constant.</li> </ol>	2x2
<b>Module 4</b>	<ol style="list-style-type: none"> <li>To draw hysteresis curve of a given sample of ferromagnetic material and from this to determine magnetic susceptibility.</li> <li>To study the Hall Effect and determine Hall coefficient, carrier density and mobility of a given semiconductor material.</li> <li>To determine the energy band gap of a given semiconductor material.</li> </ol>	4x2
<b>Total No. of Hours</b>		<b>28</b>

<b>SET/EE/BT/C107. BASIC ELECTRICAL ENGINEERING LAB</b>		
<b>Module</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Module 1</b>	<ol style="list-style-type: none"> <li>Study of analog voltmeter and ammeter.</li> <li>Study of digital multimeter.</li> <li>Study of CRO.</li> </ol>	3x2
<b>Module 2</b>	<ol style="list-style-type: none"> <li>Verification of KCL and KVL.</li> <li>Verification of Thevenin, Norton Network theorems.</li> <li>Verification of Superposition Network theorem.</li> <li>Verification of MPT Network theorem.</li> </ol>	3x2
<b>Module 3</b>	<ol style="list-style-type: none"> <li>Measurement of efficiency of a single phase transformer by load test.</li> <li>Determination of parameters and losses in single phase transformer by OC and SC test.</li> <li>Measurement of power in a three phase circuit by two wattmeter method.</li> <li>Verification of Single Phase Energy Meter constant.</li> <li>Study of three phase induction motor.</li> </ol>	5x2
<b>Module 4</b>	<ol style="list-style-type: none"> <li>Verification of junction diode, zener diode characteristics.</li> <li>Verification of Clipping and clamping circuits.</li> <li>Verification of H.W. and F.W. rectifier circuit: with and without filter circuit and to determine the ripple factor.</li> <li>Verification of CE characteristics of BJT.</li> </ol>	4x2
<b>Total No. of Hours</b>		<b>30</b>

<b>SET/IT/BT/C108. INFORMATION TECHNOLOGY LAB</b>		
<b>Module</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Module 1</b>	<ol style="list-style-type: none"> <li>Creation of a Word Document.</li> <li>Creation of a Document in spreadsheet and using Formulae.</li> <li>Use of Search Engine and World Wide Web.</li> <li>Creation of email id and email.</li> <li>Use of FTP service.</li> <li>Creation of Static Web Pages using HTML.</li> <li>Creation of Page Using Java Script.</li> </ol> <p>(Besides these additional experiments can be included to give hands on experience to students. Students can be provided opportunity to work on any Information System to give them better understanding of Information System)</p>	14x2
<b>Total No. of Hours</b>		<b>28</b>

SET/ME/BT/S109. ENGINEERING GRAPHICS		
Module Name	Content	No. of Hrs.
<b>Introduction to Engineering Graphics</b>	Drawing instruments and their use ó Different types of lines - Lettering & dimensioning ó Familiarization with current Indian Standard Code of Practice for Engineering Drawing. Scales, Plain scales, Diagonal scales, Vernier scales. Introduction to orthographic projections- Horizontal, vertical and profile planes ó First angle and third angle projections ó Projection of points in different coordinates ó Projections of lines inclined to one of the reference planes.	12
<b>Projections of lines and planes</b>	Projections of lines inclined to both the planes ó True lengths of the lines and their angles of inclination with the reference planes ó Traces of lines. Projection of plane lamina of geometric shapes inclined to one of the reference planes ó inclined to both the planes, Traces of planes. Projections on auxiliary planes.	12
<b>Projections of polyhedral and solids</b>	Projections of polyhedral and solids of revolution, projection of solids with axis parallel to one of the planes and parallel or perpendicular to the other plane ó Projections with the axis inclined to one of the planes. Projections of Solids with axis inclined to both the planes ó Projections of spheres and combination of solids.	12
<b>Sections of solids</b>	Sections of solids by planes perpendicular to at least one of the reference planes ó True shapes of sections. Developments, development of the lateral surface of regular solids like, prisms, pyramids, cylinders, cones and spheres, development of truncated solids Isometric projection ó Isometric scale ó Isometric views ó Isometric projection of prisms, pyramids, cylinders, cones, spheres and solids made by combination of the above.	12
<b>Total No. of Hours</b>		<b>48</b>
<b>Textbooks</b>	1. Bhatt N. D, Elementary Engineering Drawing, Charotar Publishing House, Anand, 2002.	
<b>References</b>	1. Narayana K L & Kanniah P, Engineering Graphics, Tata McGraw Hill, New Delhi, 1992. 2. Luzadder W J, Fundamentals of Engineering Drawing, Prentice Hall of India, New Delhi, 2001. 3. Thomas E French & Charles J V, Engineering Drawing & Graphing Technology, McGraw Hill Book Co, New York 1993. 4. Venugopal K, Engineering Drawing & Graphics, New Age International Pvt. Ltd., New Delhi, 1994.	

**SEMESTER II**

S. No.	Code	Course Title	L	T	P	T.A	C.T	TOT	ESE.	SUB. TOTAL	Credits
1	SET/SH/BT/C201	Mathematics II	3	1	-	10	20	30	70	100	3
2	SET/ME/BT/C202	Basic Mechanical Engineering	3	1	-	10	20	30	70	100	3
	SET/EE/BT/C103	Basic Electrical Engineering									
3	SET/SH/BT/C203	Chemistry	3	1	-	10	20	30	70	100	3
	SET/SH/BT/C102	Physics									
4	SET/ME/BT/C204	Engineering Mechanics	3	1	-	10	20	30	70	100	3
	SET/EC/BT/C104	Basic Electronics									
5	SET/CS/BT/C205	Computer Programming	3	1	-	10	20	30	70	100	3
	SET/IT/BT/C105	Fundamentals of Information Technology									
6	AECC206	*General English	2	-	-	10	20	30	70	100	2
7	SET/ME/BT/C206	Basic Mechanical Engineering Lab	-	-	2	30	-	30	70	100	1
	SET/EE/BT/C107	Basic Electrical Engineering Lab									
8	SET/SH/BT/C207	Chemistry Lab	-	-	2	30	-	30	70	100	1
	SET/SH/BT/C106	Physics Lab									
9	SET/CS/BT/C208	Computer Programming Lab	-	-	2	30	-	30	70	100	1
	SET/IT/BT/C108	Information Technology Lab									
10	SET/ME/BT/S209	**Engineering Workshop	-	-	4	30	-	30	70	100	2
<b>Total</b>			16	5	10	180	120	300	700	1000	22

\* Ability Enhancement Compulsory course.

\*\*Skill Enhancement Course.

L ó Lecture hours, T ó Tutorialhours, P ó Practicalhours, T.A ó Teacher's Assessment, C.T - Class Test, TOT ó Total, ESE - End Semester Examination.

<b>SET/SH/BT/C201. MATHEMATICS II</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Multiple Integral</b>	Double and triple integrals, change of order of integration. Change of variables, application to area, volume, centre of gravity, moment of inertia and product of inertia. Gamma and Beta functions, Dirichlet's integral and its application.	9
<b>Fourier Series</b>	Periodic functions, Fourier series of functions with period 2n, change of interval, half range sine and cosine series.	6
<b>Integral Transform</b>	Laplace transforms, existence theorem, Laplace transform derivatives, inverse Laplace transform, application to solve linear differential equations, unit step function, Dirac delta function, Laplace transforms of periodic functions. Application of Laplace transforms. Definitions of Fourier and Z-transform and its simple applications.	12
<b>Ordinary Differential Equations</b>	Introduction to order, degree and arbitrary constants, linear differential equations of n <sup>o</sup> order with constant coefficient, complimentary functions and particular integrals. Homogeneous differential equations, simultaneous linear differential equations. Solutions of second order differential equations by changing dependent and independent variables. Method of variation of parameters, equations of the form $\ddot{y} = f(y)$ , applications to engineering problems.	12
<b>Solutions of Equations and Curve Fitting</b>	Solutions of cubic and bi-quadratic equations. Method of least square and curve fitting.	6
<b>Total No. of Hours</b>		<b>45</b>
<b>Textbooks</b>	1. B. S. Grewal, Higher Engineering Mathematics, Khanna Publishers. 2. H K Das, Advanced Engineering Mathematics, S Chand. 3. Erwin Kreyszig, Advanced Engineering Mathematics.	
<b>References</b>	1. J. N. Kapoor, A Text Book of Differential Equations.	



SET/ME/BT/C202. BASIC MECHANICAL ENGINEERING		
Module Name	Content	No. of Hrs.
<b>Laws of Thermodynamics</b>	Concept of temperature, equality of temperature, Zeroth law, principles of thermometry and temperature scale. First law of thermodynamics, concept of internal energy, application of first law to a closed system to various processes, flow processes and control volume, flow work, steady flow energy equation, mechanical work in steady flow process, throttling process, application of first law to open system. Essence of second law, thermal reservoir, heat engines and thermal efficiency. COP of heat pump and refrigerator, definition of available and unavailable energy. Statement of second law, Carnot cycle, Carnot's theorem, Clausius inequality, concept of entropy, entropy changes for ideal gases.	8
<b>Properties of Steam</b>	Generation of steam at constant pressure, various states of water, steam, properties of steam, use of property diagram, processes of vapour in closed and open system, determination of dryness fraction of steam by separating and throttling calorimeter, Rankine cycle.	5
<b>Thermodynamic Cycle</b>	Definitions of bore, stroke, clearance ratio, compression ratio, definition and calculation of mean effective pressure from the cyclic work (proof not required), indicated pressure, air standard cycle (Otto and diesel cycle), principle of working and description of two and four stroke S.I. and C.I. engine.	8
<b>Strength of Material-Simple Stresses and Strains</b>	Stress- tensile and compressive, strain, strain energy, stress-strain diagram, ductile and brittle material, elastic constants, impact loading, varying cross-section and load, temperature stresses, shear stress, complementary shear stress, shear strain.	8
<b>Compound Stresses and Strains</b>	State of stress at a point, oblique stress, simple tension, pure shear, general two dimensional stress system, principal planes, principal stresses and strains, Mohr's stress circle, Poisson's ratio, maximum shear stress.	8
<b>Bending Stress and Torsion</b>	Pure bending, moment of inertia, section modulus, bending stresses, combined bending and direct stress, beam of uniform strength, middle third and middle quarter rules for rectangular and circular sections, Circular shafts, torsional shear stress, strain energy in torsion, shafts under varying torque, compound shafts, combined bending and twisting.	8
<b>Total No. of Hours</b>		<b>45</b>
<b>Textbooks</b>	1. R S Khurmi, "Engineering Mechanics". 2. P K Nag "Engineering Thermodynamics".	
<b>References</b>	1. Van Wylen G.J. & Sonntag R.E.: Fundamentals of classical thermodynamics, John Wiley & Sons, Inc. NY. 2. WarkWeneth : Thermodynamics (2nd edition), McGraw Hill book Co. NY. 3. Holman, J.P.: Thermodynamics, MC Graw Hill book Co. NY. 4. Yadav R.: Thermodynamics and Heat Engines, Vol I & II (SI Edition) Central Publishing House Allahabad. 5. Yadav R.: Steam & Gas Turbines. 6. Kshitish Chandra Pal: Heat Power, Orient Longman Limited, 17, Chittranjan Avenue, Calcutta. 7. S. Rao, B.B. Parulekar, "Energy Technology" Khanna Pub., New Delhi. 8. G. H. Ryder: "Strength of Materials". 9. F. L. Singer: "Strength of Materials". 10. Timoshenko: "Strength of Materials". 11. Beer, Johnson, Statics.	

SET/SH/BT/C203. CHEMISTRY		
Module Name	Content	No. of Hrs.
<b>Thermodynamics</b>	Terminology in Thermodynamics, Zeroth law of Thermodynamics, First law of Thermodynamics, Enthalpy, Reversible isothermal expansion of ideal gas, Adiabatic expansion of ideal gas, Joule-Thomson effect.	4
<b>Lubricants</b>	Theory, classification and mechanism of lubrication.	4
<b>Polymers</b>	Structures of the following polymers, viz, Natural and synthetic rubbers, Polyamide and Polyester fibres, polymethylmethacrylate, poly acrylonitrile and polystyrene. A brief account of conducting polymers (polypyrrole&polythiophene) & their applications.	3
<b>Complex Compounds</b>	Introduction, Valence bond and crystal field theory for bonding in complexes.	4
<b>Chemical Kinetics &amp; Catalysis</b>	Order and molecularity of reactions, Catalysis- homogeneous and heterogeneous catalysis. Characteristics of catalytic reactions, catalytic promoters and poisons, auto catalysis and negative catalysis. Activation energy of catalysis, intermediate compound formation theory and adsorption theory.	3
<b>Atmospheric Chemistry &amp; Air Pollution</b>	Environment and ecology, environmental segments, structure and composition of atmosphere, radiation balance of earth and Green House Effect, formation and depletion of Ozone layer, chemical and photochemical reactions of various species in atmosphere, air pollution- sources, reactions and sinks for pollutants, acid rains and smog formation. Pollution control methods.	5
<b>Corrosion</b>	Introduction, causes of corrosion, theories of corrosion- direct chemical attack, electrochemical theory of corrosion, factors influencing corrosion, passivity, types of corrosions, protection from corrosion (Cathodic and anodic protection) and protective metallic coatings (Galvanizing and tinning).	5
<b>Water and Waste Water Chemistry</b>	Introduction, Hardness of Water, Characteristics Imparted by Impurities, Determination of hardness by EDTA method, Treatment of Water by Zeolite, L-S Process, Boiler problems caused by use of hard Water, Reverse osmosis process for purification of water. Numerical based on hardness of water, zeolite process and Lime-soda process.	6
<b>Fuels &amp; Combustion</b>	Classification of Fuels, Non-Conventional Energy, Biogas, and Solar Energy, Calorific value ó Gross and Net, Characteristics of Good Fuel, Determination of Calorific Value by bomb calorimeter method (theory and numerical), Solid Fuels: Analysis of Coal (Proximate and ultimate analysis of coal theory and numerical), Liquid Fuels: mining and refining of petroleum, cracking (Thermal and catalytic), Knocking, octane and cetane number .	5
<b>Stereochemistry of Organic-Compounds</b>	Mechanism of Chemical Reaction, Beckman, Hoffman, Reimer Tiemann, Cunnizzaro, Diels- Alder and Skraup synthesis.	3
<b>Total No. of Hours</b>		<b>42</b>
<b>Textbooks</b>	1. Jain, Jain, òEngineering Chemistryö. 2. Sharma, Kumar, òEngineering Chemistryö.	
<b>References</b>	1. R. T. Morrison and R N Boyd, òOrganic Chemistryö, 6th Edition, Prentice Hall, New Delhi. 2. J. D. Lee, òConcise Inorganic Chemistryö, Chapman & Hall. 3. W. L. Jolly, òModern Inorganic Chemistryö, McGraw-Hill. 4. P.W. Atkins, òPhysical Chemistryö, 6th Edition, Oxford University Press. 5. Barrow, òPhysical Chemistryö. 6. Manahan, òEnvironmental Chemistryö. 7. D. L. Pavia, GM. Lampman, GS. Kriz and J.R Vyvyan, I, òSpectroscopyö, Cengage Learning India Pvt. Ltd, New Delhi, 2007. 8. R.M. Silverstein, F.X. Webster and D.J. Kiemle, òSpectrometric Identification of Organic Compoundsö, 7th edition, John-Wiley and Sons, New York, 2005. 9. William Kemp, òOrganic Spectroscopyö, 3rd edition, Palgrave, New York, 2005. 10. C.N. Banwell and E. M. McCash, òFundamentals of Molecular Spectroscopyö, McGraw- Hill, International, UK, 1995. 11. F. Carey, òOrganic Chemistryö, 5th Edition, McGraw Hill Publishers, Boston, 2003.	

SET/ME/BT/C204. ENGINEERING MECHANICS		
Module Name	Content	No. of Hrs.
<b>Force System</b>	Introduction: Force system, dimensions and units in mechanics, laws of mechanics, vector algebra, addition and subtraction of forces, cross and dot products of vectors, moment of a force about a point and axis, couple and couple moment, transfer of a force to a parallel position, resultant of a force system using vector method, Problems involving vector application Equilibrium: Static and dynamic equilibrium, static in determinacy, general equations of equilibrium, Varignon's theorem, Lami's theorem, equilibrium of bodies under a force system, Problems.	10
<b>Trusses And Frames</b>	Truss and Frames: Truss, classification of truss, assumptions in truss analysis, perfect truss, analysis of perfect plane truss using method of joints and method of sections, Problems.	10
<b>Centre Of Gravity And Moment Of Inertia</b>	Centroid, Centre of mass and Centre of gravity, Determination of centroid, centre of mass and centre of gravity by integration method of regular and composite figures and solid objects, Problems, Moment of Inertia: Area moment of inertia, mass moment of inertia, parallel axis and perpendicular axis theorems, radius of gyration, polar moment of inertia, product of inertia, principle axis, problem based on composite figures and solid objects.	13
<b>Kinematics And Dynamics</b>	Kinematics: Concept of rigid body, velocity and acceleration, relative velocity, translation and rotation of rigid bodies, equations of motion for translation and rotation, problems. Particle Dynamics: Energy methods and momentum methods, Newton's laws, work energy equation for a system of particles, linear and angular momentum equations, projectile motion, problem.	12
<b>Total No. of Hours</b>		<b>45</b>
<b>Textbooks</b>	<ol style="list-style-type: none"> <li>1. R S Khurmi, Engineering Mechanics.</li> <li>2. P K Nag Engineering Thermodynamics.</li> </ol>	
<b>References</b>	<ol style="list-style-type: none"> <li>1. Van Wylen G.J. &amp;Sonntag R.E.: Fundamentals of classical thermodynamics, John Wiley &amp; Sons, Inc. NY.</li> <li>2. Wark Kenneth: Thermodynamics (2nd edition), McGraw Hill book Co. NY.</li> <li>3. Holman, J.P.: Thermodynamics, McGraw Hill book Co. NY.</li> <li>4. Yadav R.: Thermodynamics and Heat Engines, Vol I &amp; II (SI Edition) Central Publishing House Allahabad.</li> <li>5. Yadav R.: Steam &amp; Gas Turbines.</li> <li>6. Kshitish Chandra Pal: Heat Power, Orient Longman Limited, 17, Chitranjan Avenue, Calcutta.</li> <li>7. S. Rao, B.B. Parulekar, Energy Technology Khanna Pub., New Delhi.</li> <li>8. G. H. Ryder: "Strength of Materials".</li> <li>9. F. L. Singer: "Strength of Materials".</li> <li>10. Timoshenko: "Strength of Materials".</li> <li>11. Beer, Johnson, Statics.</li> </ol>	

<b>SET/CS/BT/C205. COMPUTER PROGRAMMING</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Introduction</b>	C Character Set, Identifiers and Keywords, Data Types, Declarations, Expressions, Statements and Symbolic Constants.	6
<b>Operators and Expressions</b>	Arithmetic, Unary, Relational, Logical, and Assignment Operators, Conditional Operator, Library Functions.	6
<b>Control Statements</b>	While, Do-while, For Statements, Nested Loops, If-Else, Switch, Break, Continue and Go to Statements, Comma Operator.	5
<b>Functions</b>	Defining and Accessing Functions, Function Prototypes, Passing Arguments, Recursion, and Use of Library Functions.	5
<b>Program Structure</b>	Storage classes, Automatic, External, Static Variables.	4
<b>Arrays</b>	Defining and Processing, Passing to a Function, Multidimensional Arrays, Arrays and Strings.	4
<b>Pointers</b>	Declarations, Passing to a Function, Operations on Pointers, Pointers and Arrays, Dynamic Memory Allocation, Array of Pointers.	6
<b>Structures and Unions</b>	Basics of Structures, Structures and Functions, Arrays of Structures, Pointers to Structures, Self Referential Structures, type definitions, Unions.	4
<b>Data Files</b>	Open, Close, Create, Process, Unformatted data files.	4
<b>Total No. of Hours</b>		<b>44</b>
<b>Textbooks</b>	1. E. Balagurusamy, <i>Programming in ANSI C</i> .	
<b>References</b>	1. Byron S. Gottfried, <i>Programming With C</i> . 2. YashwantKanitker, <i>LET US C</i> . 3. B. W. Kernighan and D. M. Ritchie, <i>The C Programming Language</i> . 4. B. W. Kernighan, <i>The Practice of Programming</i> , Addison-Wesley, 1999. 5. C. L. Tondo and S. E. Gimpel, <i>The C Answer Book</i> , (2/e), Prentice Hall, 1988.	

<b>AECC206. GENERAL ENGLISH</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
Introduction:	Theory of Communication, Types and modes of Communication	-
Language of Communication	Verbal and Non-verbal (Spoken and Written) Personal, Social and Business Barriers and Strategies Intra-personal, Inter-personal and Group communication	-
Speaking Skills	Monologue Dialogue Group Discussion Effective Communication/ Mis-Communication Interview Public Speech	-
Reading and Understanding	Reading and Understanding Close Reading Comprehension Summary Paraphrasing Analysis and Interpretation Translation(from Indian language to English and vice-versa) Literary/Knowledge Texts	-
Writing Skills	Documenting Report Writing Making notes Letter writing	-
<b>Total No. of Hours</b>		-
<b>Textbooks</b>	<ol style="list-style-type: none"> <li>1. Fluency in English - Part II, Oxford University Press, 2006.</li> <li>2. Business English, Pearson, 2008.</li> <li>3. Language, Literature and Creativity, Orient Blackswan, 2013.</li> <li>4. Language through Literature (forthcoming) ed. Dr. Gauri Mishra, Dr Ranjana Kaul, Dr Brati Biswas</li> </ol>	

<b>SET/ME/BT/C206. BASIC MECHANICAL ENGINEERING LAB</b>		
<b>Module</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Module 1</b>	1. Study of boiler models ó Babcock Wilcox, Lancashire and Locomotive. 2. Study of Steam Engine and Steam Turbine models. 3. Study of 2-Stroke and 4-Stroke ICE models. 4. Study of vapour compression Refrigeration unit tutor. 5. Study of window type air conditioner. 6. To conduct the tensile test on a UTM and determine ultimate tensile strength, percentage elongation for a steel specimen. 7. To conduct the compression test and determine the ultimate compressive strength for a specimen. 8. To conduct impact test (Izod/Charpy) on the impact testing machine and find the impact strength. To determine the hardness of the given specimen using Brinell/Rockwell/Vicker testing machine.	15x2
<b>Total No. of Hours</b>		<b>30</b>

<b>SET/SH/BT/C207. CHEMISTRY LAB</b>		
<b>Module</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Module 1</b>	1. To determine Saponification value of given oil sample. 2. To determine the ferrous content in the supplied sample of iron ore by titrimetric analysis against standard $K_2Cr_2O_7$ solution using $K_3Fe(CN)_6$ as external indicator. 3. To determine the chloride content in supplied water sample using Mohr's method. 4. To determine acid value of given oil sample. 5. To determine the total hardness of water sample by EDTA titration. 6. To find chemical oxygen demand of a waste water sample using Potassium Dichromate. 7. Estimation of iron in plain carbon steel by redox titration. 8. Estimation of copper in brass by titration method. 9. Estimation of Zinc in brass by titration method. 10. Analysis of a coal sample by proximate analysis method.	15x2
<b>Total No. of Hours</b>		<b>30</b>

<b>SET/CS/BT/C208. COMPUTER PROGRAMMING LAB</b>		
<b>Module</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Module 1</b>	This lab shall have minimum 25 programs in C. There shall be minimum two programs per module as taught in theory. Programming shall follow logic/algorithm and flowchart wherever applicable. Exercises shall also enhance analytical and debugging abilities.	14x2
<b>Total No. of Hours</b>		<b>28</b>

<b>SET/ME/BT/C209. ENGINEERING WORKSHOP</b>		
<b>Module</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Module 1</b>	Mechanical Engineering covering, the following trades for experiments (with a minimum of two exercises under each trade) - Carpentry, Fitting, Tin-Smithy and Development of jobs carried out and soldering, Black Smithy, House Wiring, Foundry (Molding only), Plumbing.	16X2
<b>Module 2</b>	Power tools in Construction, Wood working, Electrical and Mechanical Engineering practices.	8x2
<b>Total No. of Hours</b>		<b>48</b>

**SEMESTER III**

S. No.	Code	Course Title	L	T	P	T.A	C.T	TOT	ESE.	SUB. TOTAL	Credits
1	SET/SH/BT/C301	Mathematics III	3	1	-	10	20	30	70	100	3
2	SET/EC/BT/C302	Electronic Devices and Circuits	3	1	-	10	20	30	70	100	3
3	SET/EC/BT/C303	Digital Electronics	3	1	-	10	20	30	70	100	3
4	SET/IN/BT/C304	Signals and Systems	3	1	-	10	20	30	70	100	3
5	SET/IN/BT/C305	Circuit Theory	3	1	-	10	20	30	70	100	3
6	SET/EC/BT/C306	Electronic Devices and Circuits Lab	-	-	2	30	-	30	70	100	1
7	SET/EC/BT/C307	Digital Electronics Lab	-	-	2	30	-	30	70	100	1
8	SET/IN/BT/C308	Signals and Networks Lab	-	-	4	30	-	30	70	100	2
9	SET/EC/BT/S309	Electronics Servicing Lab*	-	-	4	30	-	30	70	100	2
<b>Total</b>			15	5	15	170	100	270	630	900	21

\*Skill Enhancement Course.

L ó Lecture hours, T ó Tutorial hours, P ó Practical hours, T.A ó Teacher's Assessment, C.T - Class Test, TOT ó Total, ESE - End Semester Examination

SET/SH/BT/C301. MATHEMATICS III		
Module Name	Content	No. of Hrs.
<b>Ordinary Differential Equations</b>	ODE of 2nd order with constant coefficients both homogeneous and non-homogeneous types with applications to electrical and mechanical systems. Difference equations and their solutions by Z transform. Series solutions of ODE of 2nd orders with variable coefficients with special emphasis to the differential equations of Legendre, Bessel and Chebyshev. Legendre's polynomials, Chebyshev polynomials and Bessel's functions and their properties.	14
<b>Integral Transforms</b>	Fourier transform and integral Hankel transforms and Hilbert transforms and their properties, some simple applications.	7
<b>Partial Differential Equations</b>	Linear PDE with constant coefficients of 2nd order and their classifications. PDE of parabolic, elliptic and hyperbolic type with illustrative examples. Separation of variables method for solving PDE, such as two dimensional heat equations, wave equations and Laplace equations.	10
<b>Functions of a Complex Variable</b>	Analytic functions, Cauchy Riemann equations, harmonic functions line integral in the complex plane, Cauchy's integral theorem, Cauchy's integral formula derivatives of analytic function, Liouville's theorem, fundamental theorem of algebraic representation of a function by power series, Taylor's & Laurent series, poles & singularity of zeros. Residue theorem, conformal mapping, linear fractional transformation, special linear fractional transformations.	14
<b>Total No. of Hours</b>		<b>45</b>
<b>Textbooks</b>	1. B. S. Grewal, óHigher Engineering Mathematicsö, Khanna Publishers. 2. H K Das, óAdvanced Engineering Mathematicsö, S Chand. 3. Erwin Kreyszig, óAdvanced Engineering Mathematicsö.	
<b>References</b>	1. Paopoulis, óSignal Analysisö, TMH.	

SET/EC/BT/C302. ELECTRONIC DEVICES AND CIRCUITS		
Module Name	Content	No. of Hrs.
<b>PN Junction</b>	Carrier Statistics: Charge carriers in semiconductors, Fermi Dirac statistics, intrinsic and extrinsic semiconductors, carrier transport, mobility, conductivity, carrier life time, recombination, steady state carrier generation, quasi Fermi levels, drift and diffusion of carriers, continuity equation PN Junction: PN junction at equilibrium, Forward and reverse bias junctions, steady state conditions, forward and reverse bias, break down of junctions, Metal Semiconductor contacts: Rectifying and ohmic contacts, current voltage characteristics.	6
<b>Diodes and Applications</b>	Diode-large signal and small signal operation; Diode Circuits; Special Diodes: Zener Diode, Schottkey Diode, Photo Diode; Varactor Diode, Tunnel Diode; Light Emitting Diode.	5
<b>BJT Amplifiers</b>	BJT operation and characteristics: active mode, saturation mode; BJT Models: large signal model, transconductance, small signal model, early effect; Amplifier: input impedance, output impedance, gain; Operating point analysis and design: simple biasing, resistive divider biasing, biasing with emitter degeneration, self bias, and design procedures; Analysis and Design of different topologies: CE, CE with emitter degeneration, CB, CC (Emitter follower); Multi-stage amplifier; Bipolar Cascode amplifier, Bipolar current mirror; Bipolar differential amplifier.	10
<b>MOSFET Amplifiers</b>	MOSFET operation and characteristics: MOSFET as variable resistor, channel pinch off, derivation of I-V characteristics, triode and saturation region, transconductance; MOS device models: large signal model, small signal model, channel length modulation; comparison of Bipolar transistor and MOSFET; MOS Amplifier topologies: biasing, realizing current source, CS, CS with current source load, CS with diode connected load, CS with degeneration, CS with biasing, CG, CG with biasing, CD (source follower), CD with biasing; CMOS Cascode amplifier, MOS current mirror; MOS differential amplifier.	10
<b>Frequency Response</b>	Poles and zeroes in circuits, Bode plot, miller's theorem, high frequency models for BJT and MOSFET; transit or cut-off frequency of device; frequency response of CE and CS amplifier; bandwidth, effect of frequency on I/O impedances.	5
<b>Feedback</b>	Negative feedback: gain desensitization, bandwidth extension, modification of I/O impedances, linearity improvement; types of amplifiers: voltage, trans-impedance, trans-conductance, and current amplifiers; Sense and return techniques; polarity of feedback; feedback topologies: voltage-voltage feedback, voltage-current feedback, current-voltage feedback, current-current feedback; Stability in feedback systems: problem of instability, stability condition, Nyquist stability criterion, phase margin, frequency compensation; Barkhausen condition for Oscillations, Sinusoidal oscillators.	5
<b>Power Amplifiers</b>	Distortion and efficiency; emitter follower as power amplifier; push-pull stage, high fidelity design using feedback; heat dissipation, thermal runaway; efficiency of emitter follower and push-pull stage; power amplifier classes; transformer coupled amplifiers.	4
<b>Total No. of Hours</b>		<b>45</b>
<b>Textbooks</b>	<ol style="list-style-type: none"> <li>1. Behzad Razavi, "Fundamental of Microelectronic Circuits", Wiley.</li> <li>2. R.L. Boylestad, L. Nashelsky, "Electronics Devices &amp; Circuit Theory", PHI.</li> <li>3. B. G. Streetman, "Solid state Devices", Pearson.</li> </ol>	
<b>References</b>	<ol style="list-style-type: none"> <li>1. Millman, Halkias, "Electronic Devices and Circuits".</li> <li>2. David A. Bell, "Electronic Devices and Circuits".</li> <li>3. Sedra, Smith, "Microelectronic Circuits".</li> </ol>	



SET/EC/BT/C303. DIGITAL ELECTRONICS		
Module Name	Content	No. of Hrs.
<b>Introduction</b>	Positional number system; Binary, octal and hexadecimal number systems; Methods of base conversions; Binary, octal and hexadecimal arithmetic; Representation of signed numbers; Fixed and floating point numbers. Definition and specification of combination logic; Truth table; Basic logic operation and logic gates; Binary coded decimal codes; Gray codes.	6
<b>Boolean Algebra and Switching Functions</b>	Basic postulates and fundamental theorems of Boolean algebra; Standard representation of logic functions - SOP and POS forms; Simplification of switching functions - K-map.	4
<b>Logic Families</b>	Diode, BJT and MOSFET as a switch. Introduction to different logic families; Electrical characteristics of logic gates ó logic levels and noise margins, fan-out, propagation delay, transition time, power consumption and power-delay product; circuit description and operation; RTL; DTL,HTL,TTL and sub families , Brief idea of ECL, CMOS BI-CMOS.	10
<b>Combinational Logic</b>	Arithmetic modules: adders, subtractors and ALU; Design examples. Decoders, encoders, multiplexers and de-multiplexers; Parity circuits and comparators.	6
<b>Sequential Logic</b>	Basic sequential circuits- latches and flip-flops: SR-latch, D-latch, D flip-flop, JK flip-flop, T flip-flop and their inter-conversions; Timing hazards and races; Meta-stability; Analysis of state machines using D flip-flops and JK flip-flops; Definition of state machines, synchronous sequential logic, shift register, counters-ripple and mod counters.	12
<b>Semiconductor Memories</b>	RAM, ROM, Content Addressable Memory, Charge Coupled Device Memory. PLAs, PALs and their applications; Sequential PLDs and their applications.	4
<b>Total No. of Hours</b>		<b>42</b>
<b>Textbooks</b>	1. Morris Mano, óDigital Designö.	
<b>References</b>	1. Taub, Schilieng, óDigital Integrated Electronicsö. 2. Anad Kumar, óDigital principles and applicationö. 3. John F Wakerly, óDigital Design: Principles and Practicesö, Prentice Hall. 4. Thomas L. Floyd, óDigital Fundamentalsö, Pearson/ Prentice Hall. 5. Ronald J. Tocci, óDigital Systems: Principles and Applicationsö, Pearson/ Prentice Hall. 6. Charles Roth, óFundamentals of Logic Designö, Jaico Publishing House.	

<b>SET/IN/BT/C304. SIGNALS AND SYSTEMS</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Introduction to signals</b>	Classification of signals, basic continuous- time and discrete- time signals, step and impulse functions, transformation of independent variable. Sampling, Quantization, Encoding; Sampling theorem.	6
<b>Introduction to systems</b>	Properties of systems, classification of systems, mathematical model for systems, normal form of system equations, initial conditions; Impulse response of a physical system, Introduction to convolution, Convolution integral, numerical convolution. , auto correlation function, properties of auto correlation function, cross correlation functions, properties of cross correlation functions.	8
<b>Fourier Analysis</b>	Representation of signals in terms of elementary signals, condition for orthogonality, representation of signals by elementary sinusoids, Fourier series representation, power spectrum, Fourier Transform, system function, energy spectrum, Calculation of simple transforms, Discrete Fourier Transform (DFT), properties of Discrete Fourier Transform.	12
<b>Laplace Transform</b>	Convergence of laplace transform, Properties of laplace transform, inversion of laplace transform, solution of differential equation, bilateral laplace transform.	8
<b>Z-transform</b>	Z-transform, convergence of Z-transform, properties of Z-transform, inversion of Z-transform, evaluation of system frequency response, applications of Z-transform.	8
<b>Total No. of Hours</b>		<b>42</b>
<b>Textbooks</b>	1. Simon Haykin, Signals & Systems, John Wiley. 2. Oppenheim, Wilskey, Signals and Systems, PHI.	
<b>References</b>	1. B.P.Lathi, Linear systems and signals, OUP. 2. Paopoulis, Signal Analysis, TMH.	

SET/IN/BT/C305. CIRCUIT THEORY		
Module Name	Content	No. of Hrs.
<b>Networks and Transients</b>	Review of Network Theorems: Thevenin's & Norton's theorem - Superposition theorem - Maximum power transfer theorem & Reciprocity Theorem - Millman's theorem; Introduction to Network Topology: Definition of basic terms & Incidence matrix & Tie-sets - Cut-sets: Analysis and formulation of network equations using tie-set and cut-set; Transients in linear circuits: Initial Conditions - Zero state response - Zero input response - Complete Response & Analysis of RC and RL circuits with impressed DC voltage & RC network as differentiator and integrator - Compensated Attenuators & DC transients in RLC circuits.	11
<b>S-Domain Analysis and Network Functions</b>	S-Domain Analysis of Circuits: Review of Laplace transform - Transformation of a circuit into S-domain - Transformed equivalent of inductance, capacitance and mutual inductance - Impedance and admittance in the transform domain - Node analysis and mesh analysis of the transformed circuit; Network functions: Impulse response and Transfer function - Poles and Zeros & Restriction of pole and zero locations of network functions - Steady state response and Frequency response from Laplace transform.	12
<b>Two port networks</b>	Characterization in terms of impedance - Admittance - Hybrid and transmission parameters - Inter relationships among parameter sets - Interconnection of two port networks - Series, parallel and cascade. Symmetrical two port networks: T and $\pi$ Equivalent of a two port network. Symmetrical Two Port Reactive Filters: Filter fundamentals - Pass and stop bands - Constant - k low pass filter - Constant - k high pass filter - m-derived T and $\pi$ sections and their applications for infinite attenuation and filter terminations - Band pass and band elimination filters.	11
<b>Network Synthesis</b>	Synthesis: Positive real functions - Driving point functions - Brune's positive real functions - Properties of positive real functions. Testing driving point functions - Application of maximum modulus theorems - Properties of Hurwitz polynomials - Even and odd functions - Strum's theorem - Driving point synthesis - RC elementary synthesis operations - LC network synthesis - Properties of RC network functions - Foster and Cauer forms of RC and RL networks.	8
<b>Total No. of Hours</b>		<b>42</b>
<b>Textbooks</b>	1. D Roy Choudhary, Network and Systems, Wiley Eastern,.	
<b>References</b>	<ol style="list-style-type: none"> <li>1. Van Valkenburg M E, Network Analysis 3rd Edition, Prentice Hall.</li> <li>2. Van Valkenberg M.E., Introduction to Modern Network Synthesis, John Wiley and Sons.</li> <li>3. Franklin. F. Kuo, Network Analysis and Synthesis, John Wiley &amp; sons.</li> <li>4. Hayt, Kimmerly, Engineering Circuit Analysis, McGraw Hill.</li> <li>5. Desoer C.A. &amp; Kuh E.S., Basic Circuit Theory, McGraw-Hill.</li> <li>6. Ryder J.D., Networks, Lines and Fields, Prentice Hall.</li> <li>7. B. P. Lathi, Linear Systema and Signals, Oxford University Press.</li> <li>8. DeCarlo, R.A., &amp; Lin, "Linear Circuit Analysis", 2 nd Edition, OUP Indian Edition 2003.</li> <li>9. MahmoodNahvi, Joseph, A. Edminister, "Theory and Problems of Electric Circuits &amp; Schaum's outline series", McGraw Hill.</li> <li>10. Donald E. Scott, "An Introduction to Circuit analysis: A System Approach" McGraw Hill Book Company.</li> <li>11. A.Chakrabarti,"Circuit Theory" DhanpatRai&amp; Co.</li> </ol>	

<b>SET/EC/BT/C306. ELECTRONIC DEVICES AND CIRCUITS LAB</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Experiments</b>	<ol style="list-style-type: none"> <li>1. Clipping and clamping circuits.</li> <li>2. Half wave, Full wave rectifiers Bridge Rectifiers.</li> <li>3. BJT and JFET Biasing schemes and Bias Stability comparison.</li> <li>4. Emitter follower ó frequency and phase response.</li> <li>5. Single stage BJT amplifier ó Frequency Response.</li> <li>6. Single stage JFET amplifier ó Frequency Response.</li> <li>7. Power amplifier ó Class A, Class B, ClassAB and Class C.</li> <li>8. Two stage RC coupled amplier ó Frequency Response.</li> <li>9. Cascode Amplifier ó Frequency Response.</li> <li>10. Feedback Topologies and amplifiers.</li> <li>11. Phase Shift Oscillator.</li> <li>12. Colpitts/Hartley Oscillators.</li> <li>13. Astable, Monostable and BistableMultivibrator with BJT.</li> </ol>	10x2
<b>Spice Simulations</b>	<ol style="list-style-type: none"> <li>1. Clipping and clamping circuits.</li> <li>2. Bridge rectifier.</li> <li>3. Common emitter amplifier with voltage divider biasing- dc, transient, ac analysis.</li> <li>4. Inverting, Non-Inverting, Difference, Instrumentation Amplifiers.</li> </ol>	4x2
<b>Total No. of Hours</b>		<b>28</b>

<b>SET/EC/BT/C307. DIGITAL ELECTRONICS LAB</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Experiments</b>	<ol style="list-style-type: none"> <li>1. Combinational Logic design using basic gates (Code Converters, Comparators).</li> <li>2. Combinational Logic design using decoders and MUXs.</li> <li>3. Arithmetic circuits - Half and full adders and subtractors.</li> <li>4. Arithmetic circuits ó design using adder ICs, BCD adder.</li> <li>5. Flip flop circuit (RS latch, JK &amp; master slave) using basic gates.</li> <li>6. Asynchronous Counters.</li> <li>7. Synchronous counters, Johnson&amp; Ring counters.</li> <li>8. Sequential Circuit designs (sequence detector circuit).</li> <li>9. Transfer Characteristics , Measurement of Sinking and Sourcing currents etc. of TTL gates.</li> </ol>	10x2
<b>ModelSim Simulations</b>	Writing and simulating programs for adder, decoder, multiplexer, de-multiplexer, up/down counter, universal shift register, Sequence Detector etc.	4x2
<b>Total No. of Hours</b>		<b>28</b>

<b>SET/IN/BT/C308. SIGNALS AND NETWORKS LAB</b>		
<b>Module</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Module 1</b>	1. Programming using MATLAB.	10x2
<b>Module 2</b>	<ol style="list-style-type: none"> <li>2. Verification of principle of superposition with dc and ac sources.</li> <li>3. Verification of Thevenin, Norton and Maximum power transfer theorems in ac circuits.</li> <li>4. Verification of Telleginø's theorem for two networks of the same topology.</li> <li>5. Determination of transient response of current in RL and RC circuits with step voltage input.</li> <li>6. Determination of frequency response of current in RLC circuit with sinusoidal ac input.</li> </ol>	4x2
<b>Total No. of Hours</b>		<b>28</b>

<b>SET/EC/BT/S309. ELECTRONICS SERVICING LAB</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Experiments</b>	<ol style="list-style-type: none"> <li>1. Familiarization of instruments-voltmeters, ammeters, multimeters (analog, digital), CRO, signal generators.</li> <li>2. Familiarization of Electronics Devices- Diodes, Transistors, different types of ICs</li> <li>3. Designing and servicing of various basic electronics circuits (Half wave, Full wave rectifiers, clipper clamper circuits and power supplies).</li> <li>4. Analog and digital calibration and troubleshooting techniques.</li> <li>4. Oscilloscopes, Function Generators - Trouble shooting and servicing.</li> </ol>	24x2
<b>Total No. of Hours</b>		<b>48</b>

**SEMESTER IV**

S N	Code	Course Title	L	T	P	T.A	C.T	TOT	ESE.	SUB. TOTAL	Credits
1	SET/EC/BT/C401	Analog Integrated Circuits	3	1	-	10	20	30	70	100	3
2	SET/IN/BT/C402	Microprocessors and Interfacing	3	1	-	10	20	30	70	100	3
3	SET/EC/BT/C403	Analog Communication	3	1	-	10	20	30	70	100	3
4	SET/EC /BT/C404	Electromagnetic Field Theory	3	1	-	10	20	30	70	100	3
5	SET/IN/BT/C405	Electrical Measurements and Instrumentation	3	1	-	10	20	30	70	100	3
6	SET/EC/BT/C406	Analog Integrated Circuits Lab	-	-	2	30	-	30	70	100	1
7	SET/IN/BT/C407	Microprocessors and Interfacing Lab	-	-	2	30	-	30	70	100	1
8	SET/EC /BT/C408	Analog Communication Lab	-	-	2	30	-	30	70	100	1
9	SET/IN/BT/C409	Electrical Measurement and Instrumentation Lab	-	-	2	30	-	30	70	100	1
10	SET/EC /BT/S410	Mini Project I*			4	30		30	70	100	2
<b>Total</b>			15	5	12	200	100	300	700	1000	21

\*Skill Enhancement Course.

L ó Lecture hours, T ó Tutorialhours, P ó Practicalhours, T.A ó Teacher's Assessment, C.T - Class Test, TOT ó Total, ESE - End Semester Examination

<b>SET/EC/BT/C401. ANALOG INTEGRATED CIRCUITS</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Introduction</b>	Operational Amplifiers, DC and AC characteristics; Applications of Op-amp: Precision rectifiers, Log and antilog amplifiers, four quadrant multipliers. Instrumentation amplifier, Sample and Hold Circuits.	9
<b>Active filters</b>	Introduction to filters. Butterworth, Chebyshev & Bessel filter; LC ladder filter ó prototype & synthesis; Frequency transformation of low pass filter. Impedance converters; Gm-C filters, Active-RC Filters; Switched capacitor filter.	8
<b>Multivibrators and Pulse shaping circuits</b>	Multivibrators using op amps; 555 timer; Triggering circuits for bistable and monostable multivibrators; Programmable timer; Pulse shaping circuits.	6
<b>PLL</b>	Analog multiplexer, PLL and its applications, Frequency synthesizers, Coherent synthesizers using PLL, Direct digital synthesis, Phase noise in oscillators.	6
<b>Power supply Regulators</b>	Voltage regulators, Regulators using op amps, IC regulators, Protection circuits, Foldback current limiting, current boosting of IC regulators, switching regulators.	6
<b>DACs and ADCs</b>	D/A Converter ó General considerations, Static non-idealities and Dynamic non-idealities; Current-steering DAC ó Binary weighted DAC, Design issues, Effect of Mismatches. A/D converter ó General considerations, static and dynamic non-idealities; Flash ADC ó Basic architecture, Design issues, Comparator and Latch, Effect of non-idealities, Interpolative and folding architectures. Successive Approximation ADC; Pipeline ADC.	7
<b>Total No. of Hours</b>		<b>42</b>
<b>Textbooks</b>	1.S.Franco, Design with Operational Amplifiers and Analog Integrated Circuits (3/e) TMH, 2003. 2.R.Gayakwad, Op-amps and Linear Integrated Circuits (4/e), PHI. 3.Coughlin, Op-amps and Analog Integrated Circuits, PHI.	
<b>References</b>	1.D.A.Bell, Solidstate Pulse Circuits (4/e), PHI. 2.M.E. Van Valkenburg, Analog Filter Design, Oxford University Press, 1995. 3.R. Schaumann and M.E. Van Valkenburg, Design of Analog Filters, Oxford University Press, 2003. 4.BehzadRazavi, Principles of Data Conversion System Design, Wiley-IEEE Press, 1995. 5.Rudy J. van de Plassche, CMOS Integrated Analog-to-Digital and Digital-to-Analog Converters, Springer, 2003. 6.Choudhury, R. and Jain, S., óLinear Integrated Circuitsö, 3rd Edition.	

<b>SET/IN/BT/C402. MICROPROCESSORS AND INTERFACING</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Architecture</b>	General 8-bit microprocessor and its architecture- 8085 and similar processors, functional block diagram, architecture, functions of different sections, instruction format, addressing modes, instruction set of 8085 CPU.	8
<b>Assembly Language Programming</b>	Assembly format of 8085, assembly directives, simple programming practices, stack and subroutines.	8
<b>Timing diagrams</b>	Instruction cycle, timing diagrams, different machine cycles, fetch and execute operations, estimation of execution time. Introduction to 16 bit processor.	6
<b>Memory interface design</b>	I/O interfacing methods: I/O mapped I/O, Memory mapped I/O; Interfacing 8085 to memories: 64K x 8, 8K x8, 4Kx 8, 8K x4 interface sample designs.	4
<b>Peripheral interface ICs:</b>	Communication modes, Serial, Parallel communication, simplex, half duplex, full duplex modes; Architecture and functional description of PPI 8255, operating modes: BSR, I/O mode- Mode 0, 1 and 2; Programming 8255; Architecture and functional description of USART 8251, asynchronous and synchronous transmission/ reception, programming 8251.	8
<b>Data Transfer &amp; Interfacing</b>	Data transfer schemes, programmed I/O, interrupt structure of 8085, serial I/O, input/output ports, latches and buffers; interfacing of A/D and D/A converters, RAM, and ROM.	4
<b>DAS and Applications</b>	Data acquisition systems, temperature control, waveform generation and stepper motor control.	4
<b>Total No. of Hours</b>		<b>42</b>
<b>References</b>	<ol style="list-style-type: none"> <li>1. R. S. Goankar, "Microprocessor Architecture, Programming and Applications", Wiley.</li> <li>2. Mathur A P, "Introduction to Microprocessors", Tata McGraw Hill.</li> <li>3. P. K. Gosh &amp; P.R. Sridhar, "80000 to 8085 Introduction to Microprocessors for Engineers &amp; Scientists".</li> <li>4. Intel manual.</li> </ol>	

<b>SET/EC/BT/C403-ANALOG COMMUNICATION</b>		
<b>ModuleName</b>	<b>Content</b>	<b>No.of Hrs.</b>
<b>Introduction</b>	An overview of electronic communication system-signals and information, system block Diagram, performance metrics and data rate limits; Signal and Spectra; Orthogonal representation of signals; Random variables and processes: probability, random variables, random processes;	<b>8</b>
<b>Amplitude Modulation systems</b>	Need for frequency translation, DSB-SC modulation, DSB-C, SSB, VSB, QAM, FDM, AM and linearity, Radio Transmitter and Receiver; Superheterodyne receiver;	<b>12</b>
<b>Angle Modulation</b>	Angle Modulation, phase modulation and frequency modulation, tone modulated FM signal, arbitrary modulated FM signal, FM modulators and demodulators, approximately compatible SSB systems, PLL and applications;	<b>14</b>
<b>NoiseandCommunication</b>	Mathematical representation of Noise: sources of noise, frequency domain representation of noise, superposition of noises, linear filtering of noises, quadrature components of noise, representation of noise using orthogonal coordinates; Noise performance of AM/FM/PM systems;	<b>8</b>
<b>TotalNo. ofHours</b>		<b>42</b>
<b>Textbooks</b>	<ol style="list-style-type: none"> <li>1. Taub,Schilling, Goutam, Saha,öPrinciples of communication systemsö,3rdEdition,TMH.</li> <li>2. Singh &amp;Sapre,öCommunication System: Analog &amp; Digitalö, 2<sup>nd</sup> Edition, TMH.</li> </ol>	
<b>References</b>	<ol style="list-style-type: none"> <li>1. S. Haykin,Communication systems, John Wiley, 2001.</li> <li>2. B.P. Lathi, Analog and Digital Communication system</li> </ol>	

SET/EC/BT/C404-ELECTROMAGNETIC THEORY		
ModuleName	Content	No.of Hrs.
<b>ReviewofVector Calculus</b>	Orthogonal coordinate systems, Coordinate transformation, Gradient of scalar fields, Divergence and Curl of vector fields.	<b>3</b>
<b>Electrostatics</b>	Coulomb's law, electric field, flux and Gauss's law, curl and divergence of electrostatic fields, electric potential, Poisson's equation, Laplace's equation, solutions to electrostatic boundary problems, method of images, work and energy in electrostatics, induced dipoles and polarization, field inside a dielectric, electric displacement, electric susceptibility, permittivity and dielectric constant, boundary conditions, capacitors, surface charge and induced charge on conductors.	<b>9</b>
<b>Magneto statics</b>	Lorentz force, Biot-Savart law, magnetic flux density, divergence and curl of flux density, Ampere's law, magnetic vector potential, magnetization, torque and force on magnetic dipoles, magnetic field inside matter, magnetic field intensity, magnetic susceptibility and permeability, magnetic materials, boundary conditions	<b>9</b>
<b>Electrodynamics</b>	Electromagnetic induction, inductance, continuity equation, displacement current, Maxwell's equations, boundary conditions, Poynting's theorem, energy and momentum in electromagnetic field.	<b>6</b>
<b>Electromagnetic Waves</b>	EM waves in vacuum and in matter, mono chromatic plane waves, group velocity, wave polarization, Lorentz gauge, retarded potentials, TEM modes.	<b>5</b>
<b>Reflection and transmission at interfaces</b>	Normal and Oblique incidence of uniform plane electromagnetic waves at conducting boundary, dielectric boundary	<b>5</b>
<b>Transmission lines</b>	Quasi-TEM analysis, characteristic impedance, standing wave ratio, impedance matching techniques, Smith Chart	<b>5</b>
<b>Total No. of Hours</b>		<b>42</b>

<b>Textbooks</b>	1. Hayt W. H. & Buck R. V. <i>Engineering Electromagnetics</i> , 6th Edition, McGraw-Hill, 2006
<b>References</b>	<ol style="list-style-type: none"> <li>1. David J Griffiths: <i>Introduction to Electrodynamics</i>, Third edition, PHI, 1999</li> <li>2. David Cheng: <i>Field and Wave Electromagnetics</i>, Second edition, Pearson Education Asia, 2001</li> <li>3. Nannapaneni Narayana Rao: <i>Elements of Engineering Electromagnetics</i>, Fifth edition, PHI</li> <li>4. Matthew N. O. Sadiku: <i>Elements of Electromagnetics</i>, Fourth Edition, Oxford University Press</li> <li>5. J D Krauss: <i>Electromagnetics</i>, Fourth edition, MGH, 1992</li> </ol>



<b>SET/IN/BT/C405. ELECTRICAL MEASUREMENTS AND INSTRUMENTATION</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Electrical Instruments</b>	D'Arsonval Galvanometer. Working principle and operation of PMMC, MI, electro-dynamometer and rectifier type instruments. Wattmeters - introduction, electro-dynamics type wattmeter, theory, shape of scale, errors. Potentiometers - DC potentiometer - introduction, basic potentiometer circuit, laboratory type, multi-range, precision type, Vernier type, volt ratio box, applications. AC potentiometer - introduction, types, applications. Instrument transformers - introduction, use, ratios, burdens. Current transformers - relationships, errors. Potential transformer - introduction, relationships, errors.	16
<b>Measurements</b>	Measurement of voltage, current, power, power factor and energy. Measurement of resistance - measurement of low (Kelvin double bridge method), medium (ammeter-voltmeter, substitution, Wheatstone bridge & Ohmmeter method) and high resistance (guard circuit, direct deflection, loss of charge and Megohm bridge method) and earth resistance measurement.	16
<b>AC bridges</b>	Sources and detectors, general equation for bridge balance, general form of AC bridge. Self inductance bridges - Maxwell's inductance, Maxwell's inductance-capacitance, Hay's, Anderson and Owen's bridge. Capacitance bridges - Desauty and Schering bridges. Mutual inductance bridges & Heaviside and Campbell bridges. Frequency bridge & Wien's bridge. Sources of errors in bridge circuits.	10
<b>Total No. of Hours</b>		<b>42</b>
<b>References</b>	1. A K Sawhney, "Electrical and Electronic Measurements and Instrumentation" 2. E. W. Golding & F. E. Widdis, "Electrical Measurements and Measuring Instruments"	

<b>SET/EC/BT/C406. ANALOG INTEGRATED CIRCUITS LAB</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Experiments / Spice Simulations</b>	<ol style="list-style-type: none"> <li>1. Differential amplifier and Current Source.</li> <li>2. Measurement of Op-Amp parameters <math>\delta</math> CMRR, Slew rate, Open loop. Gain, input and output impedances, Unity gain bandwidth.</li> <li>3. Inverting non-inverting amplifiers, Integrator, Differentiator <math>\delta</math> frequency response.</li> <li>4. Instrumentation Amplifier using Op-amps and IC <math>\delta</math> Gain, CMRR and Input impedance.</li> <li>5. Op-amp in comparator application.</li> <li>6. Waveform Generators <math>\delta</math> Sine, square, Triangular and Ramp.</li> <li>7. Schmitt trigger &amp; Precision rectifiers.</li> <li>8. Astable and Monostable Multivibrators using op-amp and 555IC.</li> <li>9. Phase Locked Loops.</li> <li>10. Low Pass Filter and High Pass Filter realizations using op-amps.</li> <li>11. Band Pass Filter and Band Stop Filter realizations using op-amps.</li> <li>12. DAC and ADC circuits using op-amp/ICs.</li> <li>13. Regulated power supply using op amp IC and zener diode.</li> </ol>	15x2
<b>Total No. of Hours</b>		<b>30</b>

<b>SET/IN/BT/C407. MICROPROCESSORS AND INTERFACING LAB</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Experiments</b>	<ol style="list-style-type: none"> <li>1. Familiarization with 8085 microprocessor kit and its keyboard.</li> <li>2. Exercises with entry and manipulation of data (Different addressing modes).</li> <li>3. Programming exercises using 8085 microprocessor.</li> <li>4. Programming exercises to programmable peripheral interface.</li> <li>5. Programming exercises using interrupts. Memory Interfacing.</li> <li>6. Interfacing peripheral ICs: 8255, 8251, 8254.</li> <li>7. Interfacing Stepper motor with 8085.</li> </ol>	14x2
<b>Total No. of Hours</b>		<b>28</b>

<b>SET/EC/BT/C408. ANALOG COMMUNICATION LAB</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Experiments</b>	<ol style="list-style-type: none"> <li>1. To study AM and determine Depth of Modulation.</li> <li>2. To study generation of DSB-SC amplitude modulation using balanced modulator.</li> <li>3. To study generation of SSB amplitude modulated signal.</li> <li>4. To study envelope detector for demodulation of AM signal and observe diagonal peak clipping effect.</li> <li>5. To study amplitude demodulation by linear diode detector</li> <li>6. To study frequency modulation (FM) and determine its modulation factor</li> <li>7. To study PLL 565 as frequency demodulator.</li> <li>8. To study the Sensitivity, Selectivity, and Fidelity characteristics of super heterodyne AM receiver.</li> </ol>	3x10
<b>Total No. of Hours</b>		<b>30</b>

<b>SET/IN/BT/C409. ELECTRICAL MEASUREMENTS AND INSTRUMENTATION LAB</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Experiments</b>	1. Study of electrical instruments: MI, PMMC, Dynamometer, wattmeter. Energy meter, potentiometer and instrument transformer. 2. Calibration of instruments: AC voltmeter and ammeter. 3. Wheatstone bridge and Kelvin's Bridge for Measurement of Resistance. 4. Design, Construction and Calibration of series and shunt type Ohmmeters. 5. Schering Bridge for Capacitance Measurement and Anderson Bridge for Inductance Measurement. 6. Calibration of Single-phase Energy meter and Wattmeter. 7. Testing of Current Transformer.	15x2
<b>Total No. of Hours</b>		<b>30</b>

<b>SET/EC/BT/C410. MINI PROJECT I</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
	Mini Project shall be a bread board/simple printed board implementation of circuit/system involving dc power supply design, discrete components, analog ICs, digital ICs, opamps, relays. Students shall be encouraged to use soldering tools, measuring instruments, datasheets etc.	40
<b>Total No. of Hours</b>		<b>40</b>

**SEMESTER V**

SN	Code	Course Title	L	T	P	T.A	C.T	TOT	ESE.	SUB. TOTAL	Credits
1	SET/IN/BT/C501	Control Systems	3	1	-	10	20	30	70	100	3
2	SET/EC/BT/C502	Digital Communication	3	1	-	10	20	30	70	100	3
3	SET/IN/BT/C503	Electrical Machines	3	1	-	10	20	30	70	100	3
4	SET/EC/BT/C504	VLSI Technology	3	1	-	10	20	30	70	100	3
5	SET/EC/BT/C505	Antenna and Wave Propagation	3	1	-	10	20	30	70	100	3
6	SET/IN/BT/C506	Control Systems Lab	-	-	2	30	-	30	70	100	1
7	SET/EC/BT/C507	Digital Communication and Antenna Lab	-	-	2	30	-	30	70	100	1
8	SET/IN/BT/C508	Electrical Machines Lab	-	-	2	30	-	30	70	100	1
9	SET/EC/BT/C509	VLSI Technology Lab	-	-	2	30	-	30	70	100	1
10	SET/EC/BT/S510	Mini Project II*			4	30		30	70	100	2
<b>Total</b>			15	5	12	200	100	300	700	1000	21

\*Skill Enhancement Course.

L ó Lecture hours, T ó Tutorialhours, P ó Practicalhours, T.A ó Teacher's Assessment, C.T - Class Test, TOT ó Total, ESE - End Semester Examination

<b>SET/IN/BT/C501. CONTROL SYSTEMS</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Basics of Control</b>	System components - mechanical, hydraulic, pneumatic, electrical and electronic, servos and synchros, stepper motors, basic elements in control systems - open and closed loop system, electrical analogy of physical system, transfer function, block diagram, reduction techniques, signal flow graph.	10
<b>Time Response</b>	Time domain specifications, types of test inputs, I and II order system response, error coefficients, generalized error series, steady state error, PID controller response without and with first order system.	10
<b>Stability of Control Systems</b>	Characteristic equation, location of roots in S-plane for stability, Routh Hurwitz criterion, root locus techniques, construction.	8
<b>Frequency Response</b>	Frequency response - definition, bode plot, polar plot, gain margin and phase margin, Nyquist stability criterion and application.	8
<b>State space analysis</b>	Concepts of state, state variable and state model, state models for linear CT systems, solution of state equation, concept of controllability and observability.	6
<b>Total No. of Hours</b>		<b>42</b>
<b>Textbooks</b>	1. I. G. Nagrath, M. Gopal, óControl Systemsö.	
<b>References</b>	1. K. Ogata, öModern Control Enggö. 2. B. C. Kuo, öAutomatic Control Systemsö.	

<b>SET/EC/BT/C502-DIGITAL COMMUNICATION</b>		
<b>ModuleName</b>	<b>Content</b>	<b>No.ofHrs.</b>
<b>Elements of digital communication and information theory</b>	Model of a digital communication system, logarithmic measure of information rate, conditional entropy and redundancy, source coding, fixed and variable length codewords, source coding theorem, prefix coding and Kraft inequality, Shannon-Fano and Huffman coding for 1st, 2 <sup>nd</sup> and 3 <sup>rd</sup> order extensions, maximum entropy of a continuous source (with Gaussian distribution), entropy of bandlimited white Gaussian noise, mutual information and channel capacity of discrete memory less channel, Hartley-Shannon law.	6
<b>Sampling theory and pulse modulation</b>	Sampling theorem, signal reconstruction in time-domain, practical and flat top sampling, sampling of band pass signal, types of analog pulse modulation, method of generation and detection of PWM, PAM and PPM, spectra of pulse modulated systems.	6
<b>Waveform coding Techniques</b>	Discretization in time and amplitude, Linear quantizer, Quantization noise power calculation, signal to quantization noise ratio, non-uniform quantizer, A-law and $\mu$ -law companding, encoding and pulse code modulation, bandwidth of PCM, differential pulse code modulation, Delta modulation, Granular noise and slope overload, Adaptive delta modulation, Adaptive DPCM, comparison of PCM and DM, MPEG audio coding standard, Digital Multiplexing.	9
<b>Digital baseband transmission</b>	Line coding and its properties, NRZ and RZ types, Signaling format for unipolar, polar, bipolar (AMI) and Manchester coding and their power spectra (no derivation), HDB and B8ZS signaling, ISI Nyquist criterion for zero ISI and raised cosine spectrum; Matched filter receiver, derivation of its impulse response and peak pulse signal to noise ratio, correlation detector decision threshold and error probability for binary unipolar (on-off) signaling.	6
<b>Digital modulation techniques</b>	Types of Digital modulation, waveform of amplitude modulation, frequency and phase shift keying, method of generation and detection of coherent and non-coherent binary ASK, FSK and PSK, differential phase shift keying, quadrature modulation techniques, (QPSK and MSK) probability of error and comparison of various digital modulation techniques.	6
<b>Error control coding</b>	Error free communication over a noisy channel, Hamming sphere, Hamming distance and bound, relation b/w minimum distance and error detecting and correcting capability, linear block codes, encoding and syndrome decoding, cyclic codes, encoder and decoder for symmetric cyclic codes, convolutional codes, code tree and Trellis diagram, Viterbi and sequential decoding, burst error correction, comparison of performance	7
<b>Total No. of Hours</b>		<b>40</b>
<b>Textbooks</b>	1. Taub, Schilling, Goutam Saha, Principles of communication systems, 3rd Edition, TMH.	
<b>References</b>	1. B.P. Lathi, Modern analog and digital communication, Oxford University Press 2. Proakis J.J., Digital Communication 3. Simon Haykin, Communication System, John Wiley 4. Simon Haykin, Digital Communication, John Wiley 5. Samnugam, Digital Communication	

<b>SET/IN/BT/C503. ELECTRICAL MACHINES</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>DC Machines</b>	Constructing feature and principal of operation of shunt, series and compound generators and motors including emf equation and armature reaction. Performance characteristics of generators and motors, starting, speed control and breaking of motors. Two quadrant and four quadrant operation of motors, choice of dc motors for different applications, losses and efficiency.	14
<b>Transformers</b>	Auto transformers, Instrument transformers, three phase transformers.	6
<b>Induction motors</b>	Starters for cage and wound rotor type induction motors, speed control and breaking, single phase induction motors and methods of starting.	10
<b>Synchronous Machines</b>	Construction, emf, effect of pitch and distribution, armature reaction and determination of regulation of synchronous generators, principle of motor operation, effect of excitation on line current (V-curves).method of synchronization, typical applications of ac motors in industries.	12
<b>Total No. of Hours</b>		<b>42</b>
<b>References</b>	<ol style="list-style-type: none"> <li>1. Nagrath&amp;Kothari, Electrical Machines, Tata McGraw Hill.</li> <li>2. Bhimra, Electrical Machine.</li> <li>3. Theraja, Electrical TechonologyVol-II.</li> <li>4. Cotton H., Advance Electrical Techonology, Wheeler &amp; Co.</li> </ol>	

<b>SET/EC/BT/C504-VLSI TECHNOLOGY</b>		
<b>ModuleName</b>	<b>Content</b>	<b>No.of Hrs.</b>
<b>Introduction</b>	Introduction, Trends & Projections in IC Design & Technology. Comparison between semiconductor materials. Basics of Thick and thin Film Hybrid Technology and monolithic chips. Advantages, limitations & Classification of ICs.	7
<b>Monolithic Techniques</b>	Silicon Refining for EGS, Single Silicon Wafer Preparation & Crystal Defects, Epitaxial Process, Diffusion: FicksøLaws, Oxidation, Ion-Implantation, Photolithography, Basics of Vacuum Deposition & CVD, Etching techniques, Plasma Etching, Metallization and Isolation Techniques.	7
<b>Monolithic Components</b>	Diodes and Transistors, JFETs, MOSFETs, Resistors, Capacitors, MESFETs, Basics of VLSI CMOS technology, Reliability issues in CMOS VLSI, Latching, Electromigration.	7
<b>Assembly Techniques &amp; Packaging of VLSI Devices</b>	Introduction to packaging, Package design considerations, VLSI Assembly techniques, Packaging fabrication technology.	6
<b>Bipolar &amp; MOS Techniques</b>	Flow chart of Bipolar, NMOS and CMOS technologies. Basics of VLSI Design & Process Simulation, SUPREM.	6
<b>Surface Mount Technology (SMT)</b>	Through hole technology, Surface Mount Technology, applications & SM Components.	6
<b>Special Techniques for Modern Processes</b>	Self aligned silicides, hallow junction formation, nitride oxides etc. process flows for CMOS and bipolar IC processes.	5
<b>TotalNo. ofHours</b>		<b>44</b>
<b>Textbooks</b>	1. S.M. Sze, ò VLSI Technologyö, TMH 2. Eshraghian&Pucknell, ò Introduction to VLSIö, PHI 3. S.K. Gandhi, ò VLSI Fabrication Principlesö, John Willey & Sons 4. Botkar, ò Integrated Circuitsö, Khanna Publishers 5. D.Nagchoudhuri ò Principles of Microelectronics Technologyö PHI	
<b>References</b>	1. Carmen Capillo, òSurface Mount Technologyö. 2. S.M. Kang & Y. Leblibici, òCMOS Digital Integrated Circuits-Analysis and Designö, TMH.	

SET/EC/BT/C505-ANTENNA AND WAVEPROPAGATION		
ModuleName	Content	No.ofHours.
<b>Introduction and Antennas Basics</b>	Basic Antenna Parameters, Patterns, Beam Area (or Beam Solid Angle), Radiation Intensity, Beam Efficiency, Directivity, Gain, Resolution, Antenna Apertures, Effective Height, The radio Communication link, Fields from Oscillating Dipole, Single-to-Noise Ratio(SNR), Antenna Temperature, Antenna Impedance. Retarded Potential, Far Field due to an alternating current element Power radiated by a current element Field variation due to sinusoidal current distribution.	9
<b>Point Sources and Their Arrays</b>	Introduction, Point Source, Power Theorem and its Application to an Isotropic Source, Radiation Intensity, Arrays of Two Isotropic Point Sources, Non-isotropic but similar point sources and the principle of Pattern Multiplication, Pattern Synthesis by Pattern Multiplication. Linear Arrays of n Isotropic Point Sources of Equal Amplitude and Spacing, Linear Broadside Arrays with Non-uniform Amplitude Distributions. General Considerations.	8
<b>Electric Dipoles, Thin Liner Antennas and Arrays of Dipoles and Apertures</b>	Short Electric Dipole, Fields of a Short Dipole, Radiation Resistance of Short Electric Dipole, Thin Linear Antenna, Radiation Resistance of $\lambda/2$ Antenna, Array of Two Driven $\lambda/2$ Elements: Broadside Case and End-Fire Case. Yagi-Uda Antenna Design, Long-Wire Antennas, folded Dipole Antennas.	8
<b>Loop antennas and Slot antennas</b>	Loop Antenna. Design and its Characteristic Properties, Application of Loop Antennas, Far Field Patterns of Circular Loop Antennas with Uniform Current. <b>Introduction:</b> Slot Antennas, Horn Antennas, Helical Antennas, Log-Periodic Antenna, Micro-strip Antennas.	7
<b>Reflector Antennas</b>	Flat Sheet Reflectors, Corner Reflectors, The Parabola-General Properties, A comparison Between Parabolic and Corner Reflectors, The Paraboloidal Reflector, Reflector Types, Feed Methods for Parabolic Reflectors.	5
<b>Wave Propagation</b>	Plane Earth Reflection, Space Wave and Surface Wave; Space Wave Propagation: Field Strength Relation, Effects of Imperfect Earth, Effects of Curvature of Earth; Sky wave Propagation: structural details of the ionosphere, Wave Propagation Mechanism, Refraction and Reflection of Sky Waves by ionosphere, Ray Path, Critical Frequency, MUF, LUF, OF, Virtual Height and SkipDistance, Relation Between MUF and the Skip Distance, Multi-Hop Propagation, Wave Characteristics.	8
<b>TotalNo.ofHours</b>		<b>45</b>
<b>TextBook:</b>	1. John D Krauss, Ronald J Marhefka and Ahmad S. Khan, Antennas and Wave Propagation, Fourth Edition, Tata McGraw Hill, 2010 Special Indian	
<b>ReferenceBooks:</b>	1. K. D. Prasad, Antennas and wave propagation 2. A.R. Harish, M. Sachidananda, Antennas and Wave Propagation, Oxford University Press, 2009.	



SET/IN/BT/C506. CONTROL SYSTEMS LAB		
Module	Content	No. of Hrs.
Module 1	Related Simulations using MATLAB.	14x2
<b>Total No. of Hours</b>		<b>28</b>

SET/EC/BT/C507.DIGITAL COMMUNICATION AND ANTENNA LAB		
Module Name	Content	No. of Hrs.
Experiments	<ol style="list-style-type: none"> <li>1. Study of Pulse code modulation (PCM) and its demodulation using Bread Board.</li> <li>2. Study of delta modulation and demodulation.</li> <li>3. Study of pulse data coding techniques for NRZ formats.</li> <li>4. Study of Data decoding techniques for NRZ formats.</li> <li>5. Study of Manchester coding and Decoding.</li> <li>6. Study of Amplitude shift keying modulator and demodulator.</li> <li>7. Study of Frequency shift keying modulator and demodulator.</li> <li>8. Study of Phase shift keying modulator and demodulator.</li> <li>9. To plot the radiation pattern of a Dipole, Yagi-uda and calculate its beam width.</li> <li>10. To plot the radiation pattern of Horn, Parabolic &amp; helical antenna. Also calculate beam width &amp; element current.</li> </ol>	3x10
<b>TotalNo. ofHours</b>		<b>30</b>

SET/IN/BT/C508. ELECTRICAL MACHINES LAB		
Module	Content	No. of Hrs.
Module 1	<ol style="list-style-type: none"> <li>1. Open circuit characteristic of DC Shunt Generator.</li> <li>2. Load test on DC Shunt Generator.</li> <li>3. Speed control of DC Shunt Motor.</li> <li>4. Brake test on DC Shunt Motor.</li> <li>5. Brake test on DC Series Motor.</li> <li>6. Regulation characteristic of three - phase Alternator.</li> <li>7. Open circuit and short circuit tests on Single - phase Transformer.</li> <li>8. Load test on Single - phase Transformer</li> <li>9. Load test on three - phase Induction Motor.</li> <li>10. Brake test on Single - phase Induction Motor.</li> <li>11. <math>\phi</math> curves of Synchronous Motor.</li> </ol>	15x2
<b>Total No. of Hours</b>		<b>30</b>

SET/EC/BT/C509. VLSI TECHNOLOGY LAB		
Module	Content	No. of Hrs.
Module 1	Related Experiments with subject	14x2
<b>Total No. of Hours</b>		<b>28</b>

SET/EC/BT/C510. MINI PROJECT II*		
Module Name	Content	No. of Hrs.
	Mini Project shall be a breadboard/simple printed board implementation of circuit/system involving dc power supply design, discrete components, analog ICs, digital ICs , opamps ,relays, specially communication and antenna based projects. Students shall be encouraged to use soldering tools, measuring instruments, datasheets etc.	40
<b>TotalNo. ofHours</b>		<b>40</b>

**SEMESTER VI**

SN	Code	Course Title	L	T	P	T.A	C.T	TOT	ESE.	SUB. TOTAL	Credits
1	SET/EC/BT/C601	Digital Signal Processing	3	1	-	10	20	30	70	100	3
2	SET/EC/BT/C602	Microwave Engineering	3	1	-	10	20	30	70	100	3
3	SET/EC/BT/C603	Microcontrollers and Applications	3	1	-	10	20	30	70	100	3
4	SET/EC/BT/C604	Power Electronics	3	1	-	10	20	30	70	100	3
5	SET/EC/BT/C605	Computer System Architecture	3	1	-	10	20	30	70	100	3
6	SET/EC/BT/C606	Digital Signal Processing Lab	-	-	2	30	-	30	70	100	1
7	SET/EC/BT/C607	Microwave Engineering Lab	-	-	2	30	-	30	70	100	1
8	SET/EC/BT/C608	Microcontrollers and Applications Lab	-	-	2	30	-	30	70	100	1
9	SET/EC/BT/C609	Seminar	-	-	2	30	-	30	70	100	1
10	SET/EC/BT/S610	Mini Project III*			4	30		30	70	100	2
<b>Total</b>			15	5	12	200	100	300	700	1000	21

\*Skill Enhancement Course.

L ó Lecturehours, T ó Tutorialhours, P ó Practicalhours, T.A ó Teacher's Assessment, C.T - Class Test, TOT ó Total, ESE - End Semester Examination.

<b>SET/EC/BT/C601. DIGITAL SIGNAL PROCESSING</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Discrete Time Signals and Systems</b>	Discrete time signals, discrete systems, difference equations, Discrete time Fourier transform (DTFT), Properties of DTFT, frequency domain representation of LTI systems, Sampling and reconstruction of analog signals.	4
<b>Z- Transforms</b>	Bilateral z-transform, important properties of the z-transforms, inverse z-transform, system representation in the z-domain, Implementation of discrete time systems, solution of the difference equations.	6
<b>Discrete Fourier Transform</b>	Discrete Fourier transform, properties of the discrete Fourier transform, linear & circular convolution using DFT, Fast Fourier Transform algorithm, inverse DFT using FFT algorithm.	10
<b>Digital Filter Structures</b>	Characteristics of prototype analog filters, analog-to-digital filter transformations, Basic elements, IIR filter structure, FIR filter structure, lattice filter structures.	10
<b>Filter Design</b>	Design of IIR & FIR filters; Butterworth Chebyshev and Elliptic Approximations; Lowpass, Bandpass, Bandstop and High pass filters, properties of linear-phase FIR filters, window design techniques, Park-McClellan's method.	12
<b>Total No. of Hours</b>		<b>42</b>
<b>References</b>	1. A. Shalivahan, Digital Signal Processing; TMH. 2. A.V. Oppenheim & R.W. Schafer; Digital Signal Processing, Prentice Hall. 3. L.R. Rabiner & B. Gold; Theory and Applications of Digital Signal Processing, PHI. 4. A. Antoniou; Introduction of Digital Filters. 5. C. Emmanuel Ifeakor & W. Jervis Barrie; Digital Signal Processing, A Practical Approach. 6. Vinay K. Ingle & John G. Proakis ; Digital Signal Processing.	

<b>SET/EC/BT/C602- MICROWAVE ENGINEERING</b>		
<b>Units</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Propagation through Waveguide and cavity resonator</b>	Rectangular waveguide, solutions of wave equation in rectangular co-ordinates, derivation of field equations for TE and TM modes degenerate and dominant mode, power transmission and power loss, Excitation of waveguides, nonexistence of TEM mode in waveguide, introduction to circular waveguides, strip line and micro-strip line. Rectangular and cylindrical cavities. Quality factor, excitation of cavities.	12
<b>Microwave components</b>	Waveguide coupling, bends and twists, transitions, directional couplers, matched road, attenuators and phase shifters, E-plane, H-plane, and hybrid Tee, hybrid ring, wave guide discontinuities, windows, irises and tuning screws, detectors, wave meters, isolators and circulators, Scattering matrix.	8
<b>Microwave measurements</b>	Measurements of frequency, wave length, VSWR, impedance, attenuation, low and high power. Limitations of measurements using conventional active devices at microwave frequency.	8
<b>Microwave tubes</b>	Klystron, reflex klystron, magnetron, TWT, BWO: their schematic, principle of operation, performance characteristics and application.	7
<b>Microwave semiconductor devices</b>	PIN, tunnel diode, Gunn diode, IMPATT and TRAPATT, their principle of operation characteristics and application.	7
<b>Total No. of Hours</b>		<b>42</b>
<b>References</b>	<ol style="list-style-type: none"> <li>1. Liao S. Y. , δMicrowave devices and circuitsö</li> <li>2. Pozar, δMicrowavesö</li> <li>3. Collin R.E., δFoundations of Microwave engineeringö</li> </ol>	

<b>SET/EC/BT/C603. MICROCONTROLLERS AND APPLICATIONS</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Introduction</b>	Fundamental differences of microprocessors and microcontrollers. Introduction to 8051 $\mu$ C.	2
<b>Architecture and instruction set.</b>	Introduction of Architecture and instruction set. Addressing modes and data transfer. Special function registers (SFR), I/O ports. 8051 operational code mnemonics: Introduction, 8051 instruction hexadecimal codes, internal RAM and SFR addresses.	15
<b>Counters and timers in 8051</b>	Introduction, applications and Programming of Counters and Timers.	10
<b>Applications</b>	Interfacing - interfacing of switches, thumb wheel switches, seven segment displays, DAC and ADC to microcontroller, LCD interfacing, RTC interfacing.	15
<b>Total No. of Hours</b>		<b>42</b>
<b>Textbooks</b>	1. Kenneth J. Ayala : The 8051 Microcontroller, Penram International. 2. Muhammad Ali Majidi& Janice G. Majidi: The 8051 Microcontroller and Embedded Systems, Pearson.	
<b>References</b>	1. Tim Wilmshurst, An introduction to the design of small-scale embedded systems, Palgrave. 2. Jack Ganssle, The Art of Designing Embedded Systems, Elsevier, 1999. 3. J.W. Valvano, Embedded Microcomputer System: Real Time Interfacing, Brooks/Cole, 2000. 4. R. Gupta, Co-synthesis of Hardware and Software for Embedded Systems, Kluwer 1995.	

<b>SET/EC/BT/C604. POWER ELECTRONICS</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Characteristics of Power Devices</b>	Characteristics of SCR, DIAC, TRIAC, SCS, GTO, PUJT, power transistors, power FET's LASCR, two transistors model of SCR, protection of thyristors against over voltage and over current, dv/dt and di/dt. Commutation Circuits - Turn on circuits for SCR triggering with single pulse and train of pulses - synchronizing with supply, triggering with microprocessor, forced commutation - different techniques, series and parallel operation of SCR.	16
<b>Converter Single <math>\Phi</math></b>	Converters - single phase, half controlled and fully controlled rectifiers, waveforms of load voltage and line current under constant load current, dual converter.	10
<b>Inverters Single <math>\Phi</math></b>	Line commutated and forced commutated inverters, voltage source and current source inverters, parallel inverter, series inverter, PWM inverters, AC & DC choppers, step-up and step-down, cyclo converters. Typical application - AC and DC motor speed control, battery charger, switching mode power supply, uninterruptible power supply, induction and dielectric heating.	16
<b>Total No. of Hours</b>		<b>42</b>
<b>Textbooks</b>	1. P.S.Bhimra, Power Electronics. 2. M.H. Rashid, Power Electronics.	
<b>References</b>	1. N. Mohan, T.M. Undeland & W.P. Robbins, Power Electronics. 2. M.D. Singh & K.B. Khanchandani, power electronics.	

SET/EC/BT/C605.COMPUTER SYSTEMARCHITECTURE		
Module Name	Content	No. of Hrs.
<b>Introduction</b>	Introduction and overview of computer architecture, basic computer organization, register transfer notation. General aspects of processor design, CPU organization, instruction set architecture, data types, addressing modes, program sequencing.	6
<b>Instructions and Assembly language Programming</b>	Direct, indirect, indexed, relative and immediate addressing mode. Pre and post indexing, instruction formats, zero, one, two and three address machine, different types of instructionsó memory and non-memory reference instructions; Assembly languageóBasic I/O operationsó Stacks and Queues; Assembler, Compiler, Linker;	6
<b>Arithmetic</b>	Basic structure functional blocks, register involved, fetch and execution cycle, instruction sequencing; ALU design: computer arithmetic, fixed and floating points arithmetic, logical operations; design of fast adders ,multiplication and division circuits;	6
<b>Control unit</b>	Control unit concepts, execution of complete instructions, sequencing of control signals, hardware control unit, general micro-programming concepts, micro-programmed control unit, micro-instructions and their encoding.	6
<b>Pipelining</b>	Basic Concepts, Data hazards, Instruction hazards, Influence on Instruction sets; Data path and control considerationó Super scalar operation.	6
<b>Memory System Design</b>	Memory hierarchy, system balance consideration, Speed, size and cost; memory I/O design, cache, ROM, Performance consideration, Virtual memory, Memory management requirements, Secondary storage;	6
<b>Input-Output Organization</b>	Addressing I/O devices, data transfer synchronization, interrupt handling, I/O channels, computer peripherals and interfacing, I/O interfaces, I/O devices, terminals, card readers, I/O processors, Standard I/O Interfaces (PCI, SCSI, USB).	6
<b>Total No. of Hours</b>		<b>42</b>
<b>Textbooks</b>	<ol style="list-style-type: none"> <li>1. MorisM Mano,óComputer SystemArchitectureó,PHI</li> <li>2. Hamacher,C.,Vranesic,Z.andZaky, S., óComputer OrganizationóMcGraw Hill</li> </ol>	
<b>References</b>	<ol style="list-style-type: none"> <li>1. Hennesy,Patterson,óComputer OrganizationandDesign:thehardware/softwareinterfaceó,MorganKauffman</li> <li>2. JohnD.Carpenilli,óComputer Systems OrganizationandArchitectureó</li> </ol>	

<b>SET/EC/BT/C606. DIGITAL SIGNAL PROCESSING LAB</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Simulations</b>	1. MATLAB simulation for digital filters.	9x2
<b>DSP Processor and Programming</b>	1. Familiarization with DSP processor kit. 2. Familiarization with software for programming the DSP processor.	6x2
<b>Total No. of Hours</b>		<b>30</b>

<b>SET/EC/BT/C607- MICROWAVE ENGINEERING LAB</b>		
<b>ModuleName</b>	<b>Content</b>	<b>No.of Hrs.</b>
	Related Experiments	3 x 10
<b>TotalNo. ofHours</b>		<b>30</b>

<b>SET/EC/BT/C608.MICROCONTROLLERS AND APPLICATIONS LAB</b>		
<b>Module</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Module 1</b>	1. Study of 8051 kit 2. Study of KEIL(or any other 8051 compiler), Flash Programmer. 3. 8051 Assembly Language Programming Exercises. 4. Controlling LEDs using microcontroller and programming. 5. Interfacing ADC with Microcontroller and programming. 6. Interfacing DAC with Microcontroller and programming. 7. Interfacing Stepper motor with 8051 and programming. 8. LCD Display Interface with 8051 and programming.	15x2
<b>Total No. of Hours</b>		<b>30</b>

<b>SET/EC/BT/C609. SEMINAR</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
	Every Student shall deliver a seminar for 30 minutes. Topic for the seminar shall be decided in consultation with faculty. Topic can be related to an application or a technology which makes use of Electronics And Communication engineering. Students should search for the related literature and prepare a presentation. Evaluation shall be based on content, presentation and active participation.	-
<b>Total No. of Hours</b>		<b>-</b>
<b>References</b>	1. Internet and Journals/Magazines	

<b>SET/EC/BT/S610. MINI PROJECT III*</b>		
<b>Module</b>	<b>Content</b>	<b>No. of Hrs.</b>
	Mini Project shall be a printed board implementation of circuit/system involving dc power supply design, discrete components, analog ICs, digital ICs, op amps, relays etc. Project must be based on electronics, signal conditioning, communication, Microprocessor and Microcontroller.	24x2
<b>Total No. of Hours</b>		<b>48</b>

**SEMESTER VII**

SN	Code	Course Title	L	T	P	T.A	C.T	TOT	ESE.	SUB. TOTAL	Credits
1	SET/SH/BT/C701	Principles of Management	3	1	-	10	20	30	70	100	3
2	SET/EC/BT/C702	VLSI Deign	3	1	-	10	20	30	70	100	3
3	SET/EC/BT/C703	Optical Fiber Communication	3	1	-	10	20	30	70	100	3
4		Elective I	3	1	-	10	20	30	70	100	3
5		Elective II	3	1	-	10	20	30	70	100	3
6	SET/EC/BT/C710	VLSI Deign Lab	-	-	2	30	-	30	70	100	1
7	SET/EC/BT/C711	Optical Fiber Communication Lab	-	-	2	30	-	30	70	100	1
8	SET/EC/BT/C712	Project Preparation	-	-	2	30	-	30	70	100	1
9	SET/EC/BT/C713	Industrial Training Seminar*	-	-	2	30	-	30	70	100	1
<b>Total</b>			15	5	12	200	100	300	700	1000	21

\*Skill Enhancement Course.

L ó Lecturehours, T ó Tutorialhours, P ó Practicalhours, T.A ó Teacher's Assessment, C.T - Class Test, TOT ó Total, ESE - End Semester Examination.

Elective I	S. No.	Code	Course Title
	1	SET/EC/BT/E704	Advance Semiconductor Devices
	2	SET/EC/BT/E705	Telecommunication Switching
	3	SET/EC/BT/E706	Digital Image Processing

Elective II	S. No.	Code	Course Title
	1	SET/EC/BT/E707	Data Communication and Networking
	2	SET/EC/BT/E708	FPGA Based Digital Design
	3	SET/EC/BT/E709	Information Theory and Coding

SET/SH/BT/C701. PRINCIPLES OF MANAGEMENT		
Module Name	Content	No. of Hrs.
<b>General Management</b>	Nature, scope and significance of management. Process and functions of management. Overview of the functional areas of the general management.	4
<b>Financial Management</b>	Traditional and modern concept of finance function, nature, scope and significance of finance and financial management, functions of financial managers and financial decisions, financial environment.	4
<b>Marketing Management</b>	Nature, concept, scope and significance of marketing management, functions of marketing management, marketing planning and marketing mix.	4
<b>Product Development</b>	Concept, nature, significance of product management, product value, types of products, new product development, product life cycle, functions of product managers.	4
<b>Human Resource Management</b>	Concept, nature, scope, importance of human factor in managing modern organizations, functions of human resource mangers; Planning, organizing, directing, motivation, control and co-ordination.	4
<b>Operations Management</b>	Concept of operations management, tools and techniques: PERT, CEPM, JIT, KANBAN, Inventory management, six sigma, TQM, SCM.	4
<b>Production Management</b>	Concept, nature and significance of production management, functions of production managers.	4
<b>Total No. of Hours</b>		<b>28</b>
<b>Textbooks</b>	1. B. S. Goyal, Production and Operations Management.	
<b>References</b>	1. O. D. W. Koontz, Elements of Management. 2. T. N. Chabara, Principles and Practice of Management. 3. M. Y. Khan, Financial Management. 4. I. M. Pandey, Financial Management. 5. P. Kotler, Marketing Management: Analysis. 6. E. B. Flippo, Principles of Personnel Management.	



SET/EC/BT/C702- VLSIDESIGN		
ModuleName	Content	No.of Hrs.
<b>Introduction to CMOS</b>	Historical perspective and Moore's law; CMOS logic; CMOS fabrication: n-Well process; twin well process; CMOS layout: CMOS inverter layout, layout design rules-well rule, transistor rule, contact rule; Design partitioning; Logic, Circuit and Physical design; Design verification;	5
<b>MOSFET Characteristics and Models</b>	Structure and operation of MOSFET, I-V Characteristics, Channel Length Modulation, Body effect; MOSFET Scaling and Short Channel effects, Narrow channel effects; MOSFET capacitances; Level 1, 2, 3 and BSIMSPICE models for MOSFET; device characterization.	5
<b>CMOS Inverter</b>	Static and Dynamic behavior of CMOS inverter; Estimating delay for CMOS gates and interconnect: Static and Dynamic power consumption.	5
<b>Combinational Circuit families</b>	Static CMOS, ratioed circuits, dynamic circuits: domino logic, pass transistor circuits: CMOS with transmission gates; comparison of circuit families; low power CMOS circuits; speed power product.	5
<b>Sequential circuits</b>	Timing Constraints: max-delay constraint, min-delay constraint, meta-stability; clock skew; conventional CMOS latches and flip-flops, resettable latches and flip-flops, enabled latches and flip-flops;	6
<b>Interconnect</b>	Interconnect parameters and models: ideal wire, lumped model, Lumped RC model, distributed RC model, transmission line model;	5
<b>Data path Subsystem</b>	Adder, Shifter, Multiplier architectures, booth encoding;	8
<b>Memories</b>	SRAM cell read-write operation, DRAM cell, ROM: NORROM, NAND ROM, PROM, EPROM, EEPROM, Flash;	5
<b>TotalNo. ofHours</b>		<b>44</b>
<b>Textbooks</b>	<ol style="list-style-type: none"> <li>1. NeilH.Weste,DavidHarris,AyanBanerjee,CMOS VLSI Design, a circuitsand systems perspective,Pearson.</li> <li>2. JohnP Uyemura,Introduction to VLSI Systems</li> <li>3. S.M.Kang and Y. Leblebici,CMOS Digital Integrated Circuits ,McGrawHill</li> </ol>	
<b>References</b>	<ol style="list-style-type: none"> <li>1. Pucknell,BasicVLSIDesign</li> <li>2. JanM.Rabaey, A. Chandrakasan, andB.Nikolic,Digital IntegratedCircuits:AdesignPerspectivePearsonEducation</li> <li>3. MichalJohnSebastiansmith,Application-SpecificIntegratedCircuits ,Pearson</li> <li>4. WayneWolf,ModernVLSIDesign: IPbaseddesign,Prentice Hall</li> </ol>	

<b>SET/EC/BT/C703- OPTICAL FIBER COMMUNICATION</b>		
<b>Units</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Introduction</b>	Block diagram of optical fiber communication system, Advantages of optical fiber communication, Optical fiber waveguides: structure of optical wave guide, light propagation in optical fiber using ray theory, acceptance angle, numerical aperture, skew rays, wave theory for optical propagation, modes in a planar and cylindrical guide, mode volume, single mode fibers, cutoff wavelength, mode field diameter, effective refractive index and group and mode delay factor for single mode fiber	8
<b>Transmission Characteristics of Optical fiber</b>	Attenuation in optical fibers, intrinsic and extrinsic absorption, linear and nonlinear scattering losses, fiber bends losses. Dispersion and pulse broadening, intramodal and intermodal dispersion for step and graded index fibers, modal noise, over all fiber dispersion for multimode and monomode fiber, dispersion shifted fibers, modal birefringence and polarization maintaining fibers	8
<b>Optical Sources</b>	Basic concepts, Einstein relations and population inversion, optical feedback and threshold conditions, direct and indirect band gap semiconductors, spontaneous and stimulated emission in p-n junction, threshold current density, Hetero junction & DH structure, semiconductor injection lasers structure & Characteristics of injection laser. Drawback and advantages of LED and LASER, LED structures and Characteristics.	8
<b>Optical detectors</b>	Requirement for photo detections, p-n photodiode, characteristics of photo detections, p-i-n and avalanche photodiodes, phototransistors & photoconductors, receiver performance considerations Noise sources in optical fiber communication, noise in p-n, p-i-n and APD receivers, Receiver structures.	8
<b>Optical fiber communication systems</b>	Principal components of an optical fiber communication system, optical transmitter circuits, LED and laser drive circuits, optical receiver block diagram, simple circuits for pre-amplifier, automatic gain control and equalization, Regenerative repeater, BER of optical receiver, channel losses, ISI penalty and optical power budgeting for digital optical fiber system, line coding, Direct intercity and sub carrier intensity modulation using AM, FM and PM. Block diagram and detection principle of coherent optical fiber system, WDM	10
<b>Total No. of Hours</b>		<b>42</b>
<b>References</b>	<ol style="list-style-type: none"> <li>1. Senior, "Optical Fiber Communication"</li> <li>2. G.E. Keiser, "Optical Fiber Communication"</li> </ol>	

<b>SET/EC/BT/E704- ADVANCE SEMICONDUCTOR DEVICES</b>		
<b>Units</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Review of Semiconductors</b>	Semiconductor Materials and their properties, Carrier Transport in Semiconductors, Excess Carriers in Semiconductor	10
<b>Junctions and Interfaces</b>	Description of p-n junction, Action, The Abrupt Junction, Example of an Abrupt Junction, The linearly graded Junction. The Ideal Diode Model, Real Diodes, Temperature Dependence of I-V Characteristics, High Level Injection Effects, Example of Diodes. Description of Breakdown Mechanism, Zener and Avalanche Breakdown in p-n Junction	8
<b>Majority Carrier Diodes</b>	The Tunnel Diode, The Backward Diode, The Schottkey Barrier Diode, Ohmic Contacts Heterojunctions.	6
<b>Microwave Diodes &amp; Optoelectronic Devices</b>	The Varactor Diode, The p-i-n Diode, The IMPATT Diode, TRAPATT Diode, The BARITT Diode, Transferred Electron Devices. The Solar Cell, Photo detectors, Light Emitting Diodes, Semiconductor Lasers.	8
<b>MOSFETs &amp; Charge Coupled Devices</b>	Basic Types of MESFETs, Models for I-V Characteristics of Short Channel MESFETs, High Frequency Performance, MESFET Structures. Basic Structures and the Operating Principle, I-V Characteristics, Short-Channel Effects, MOSFET Structures, Charge Coupled Devices.	8
<b>Total No. of Hours</b>		<b>40</b>
<b>Textbooks</b>	M.S. Tyagi, "Introduction To Semiconductor Materials And Devices", John Willy-India Pvt. Ltd.	
<b>References</b>	1. S. M. Sze, "Physics of Semiconductor Devices", 2nd Edition, John Willy-India Pvt. Ltd. 2. B. G. Streetman and S. Banerjee, "Solid state electronics devices", 5th Edition, PHI.	

SET/EC/BT/E705- TELECOMMUNICATION SWITCHING		
Units	Content	No. of Hrs.
<b>Introduction</b>	Electronic switching systems: basics of a switching system ó stored program control ó centralized SPC and distributed SPC, space division switching ó strictó sense non-blocking switches ó rearrangeable networks, Synchronous transfer mode- asynchronous transfer mode - time division switching ó TSI operation.	10
<b>switching networks</b>	Multi stage switching networks: Two dimensional switching, Multi-stage time and space switching, implementation complexity of the switches - blocking probability analysis of multistage switches ó Lee approximation - improved approximate analysis of blocking switch - examples of digital switching systems (e.g. AT and T No.5 ESS)	11
<b>Traffic Analysis</b>	Traffic measurements, arrival distributions, Poisson process, holding/service time distributions, loss systems, lost calls cleared ó Erlang-B formula, lost calls cleared model with finite sources, delay systems, Littleø theorem, Erlang-C formula, M/G/1 model, non-preemptive priority models.	11
<b>Signaling</b>	Customer line signaling - outbandsignaling ó inbandsignaling - PCM signalling - inter register signaling ó common channel signaling principles-CCITT signaling system ó signalling system performance.	6
<b>ATM switching</b>	Introduction to ATM switching ó Fast packet switching ó self Routing switches ó Banyan network ó ATM switches ó Design of typical switches.	4
<b>Total No. of Hours</b>		<b>42</b>
<b>References</b>	1. Viswanathan T., øTelecommunication Switching Systems and Networksø, Prentice Hall of India 2. John C. Bellamy, øDigital Telephonyø Wiley Inter Science Publications 3. Schwartz M., øTelecommunication Networks - Protocols, Modeling and Analysisø, Pearson 4. Joseph Y Hui, " øSwitching and Traffic Theory for Integrated Broadband Networksø, Kluwer Academic Publishers. 5. Flood J.E., øTelecommunications Switching Traffic and Networksø, Pearson Education 6. C.Dhas, V.K.Konangi and M.Sreetharan, øBroadband Switching, architectures, protocols, design and analysisø, IEEE Computer society press, J. Wiley and Sons INC. 7. Freeman R.L., øTelecommunication System Engineeringø, John Wiley and Sons, 1989 8. Tarmo Anttalai, ø Introduction to telecommunication network engineeringø, 2 <sup>nd</sup> edition, Artech House, 2003 9. T.N.Saadawi, M.H.Ammar, A.E.Hakeem, øFundamentals of Telecommunication Networksø, Wiley Interscience 10. R.A.Thompson, øTelephone switching Systemsø, Artech House Publishers 11. Das J, øReview of Digital Communication 'State of the Art' in Signalling Digital Switching and Data Networksø, Wiley Eastern Ltd., New Delhi, 1988.	

SET/EC/BT/E706-DIGITAL IMAGE PROCESSING		
Module Name	Content	No. of Hrs.
<b>Module 1</b>	Digital image representation: Basic ideas in digital image processing: problems and applications - Image representation and modeling Sampling and quantization - Basic relationships between pixels - Two dimensional systems - shift in variant linear systems - Separable functions; 2-D convolution; 2-D correlation. Image perception - light, luminance, brightness and contrast - MTF of the visual system - visibility function - monochrome vision models - image fidelity criteria - colour representation - colour matching and reproduction - colour co-ordinate systems - colour difference measures - colour vision models.	8
<b>Module 2</b>	Image transforms: 2-D Discrete Fourier transform - properties; Walsh Hadamard, Discrete Cosine, Haar and Slant transforms; The Hotelling transform. Matrix theory - block matrices and Kronecker products - Circulant matrix formulation for complexity reduction; Algebraic methods - random fields - spectral density function -	8
<b>Module 3</b>	Image enhancement & Restoration: Image enhancement: Basic gray level transformations ó Histogram processing: histogram equalization and modification - Spatial operations - Transforms operations - Multispectral image enhancement - Colour image enhancement Image restoration: Degradation model; Restoration in presence of noise only ó Estimating the degradation function - Inverse _filtering - Wiener _filtering ó Constrained Least Squares filtering.	10
<b>Module 4</b>	Image compression: Fundamental concepts of image compression - Compression models - Information theoretic perspective - Fundamental coding theorem ó Lossless Compression: Huffman Coding- Arithmetic coding ó Bit plane coding ó Run length coding - Lossy compression: Transform coding ó Image compression standards.	9
<b>Module 5</b>	Image segmentation: Detection of Discontinuities ó Edge linking and boundary Description: Local processing ó Global processing ó Hough transform ó Thresholding ó Region based segmentation.	7
<b>Total No. of Hours</b>		<b>42</b>
<b>References</b>	<ol style="list-style-type: none"> <li>1. R. C. Gonzalez, R. E. Woods, Digital Image Processing, Pearson Education. II Ed.,2002</li> <li>2. Jain A.K., "Fundamentals of Digital Image Processing," Prentice-Hall, 1989.</li> <li>3. Jae S. Lim, Two Dimensional Signal And Image Processing, Prentice-Hall, Inc, 1990.</li> <li>4. Pratt W.K., "Digital Image Processing", John Wiley, 1991.</li> <li>5. K. R. Castleman, .Digital image processing., Prentice Hall, 1995.</li> <li>6. Netravalli A.N. &amp;Hasbell B.G., "Digital Pictures-Representation Compression and Standards", Plenum Press, New York, 1988.</li> <li>7. Rosenfeld &amp;Kak A.C., "Digital Picture Processing", Vol.1&amp;2, Academic Press, 1982.</li> </ol>	

<b>SET/EC/BT/E707-DATA COMMUNICATION AND NETWORKING</b>		
<b>Units</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Introduction to networks</b>	Networks: Components and Categories, Types of Connections, Topologies, Transmission Media, Coaxial Cable, Fiber Optics, ISO/OSI Model.	8
<b>Data link layer</b>	Error- Detection and correction, Parity, LRC, CRC, Hamming code, Low Control and Error control, Stop and wait, ARQ, Sliding window, HDLC, LAN, IEEE 802 Standards, Wireless LAN, Bridges.	8
<b>Network layer</b>	Inter-networks, Packet Switching and Datagram approach, IP Addressing methods, Sub-netting, Routing, Distance Vector Routing, Link State Routing, Routers.	8
<b>Transport layer</b>	Duties of transport layer, Multiplexing, De-multiplexing, Sockets, User Datagram Protocol (UDP), Transmission Control Protocol (TCP), Congestion Control, Quality of Services (QOS)	8
<b>Application layer</b>	Domain Name Space (DNS), SMTP, FTP, HTTP & WWW, Network Security.	4
<b>Industrial Data Networks</b>	RS 485 AND RS 422, 20ma current loop & Serial interface converters; MODBUS protocol, Data highway (plus) protocol; HART Protocol; Introduction to AS interface and Device-Net; Introduction to Profibus; Foundation fieldbus versus Profibus; 10Mbps Ethernet; 100Mbps;	6
<b>Total No. of Hours</b>		<b>42</b>
<b>Textbooks</b>	1. Behrouz A. Forouzan, "Data communication and Networking". Tata McGrawHill, 2004 2. Mackay, S., Wrijut, E., Reynders, D. and Park, J., "Practical Industrial Data Networks Design, Installation and Troubleshooting", Newnes Publication, Elsevier, 1st Edition, 2004.	
<b>References</b>	1. Andrew S. Tanenbaum, "Computer Networks". PHI, Fourth Edition, 2003. 2. William Stallings, "Data and Computer Communication", Sixth Edition, Pearson Education 3. Leon-Garcia, Widjaja: Communication Networks, TMH. 4. Buchanan, W., "Computer Busses", CRC Press, 2000 5. Stallings, W., "Wireless Communication and Networks", 2nd Edition, Prentice Hall of India.	

<b>SET/EC/BT/E708- FPGA BASED DIGITAL DESIGN</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>FPGAs</b>	Role of FPGAs, Types of FPGAs, FPGA vs. Custom VLSI, FPGA based system design; FPGA Fabrics: FPGA architectures, SRAM based FPGAs- logic elements, interconnection networks, configuration, Permanently programmed FPGAs- antifuses, Flash configuration, logic blocks, interconnection networks, programming, Chip I/O; Architecture of FPGA fabrics- logic element parameters, interconnect architecture, pinout; Modeling with HDLs: Simulation algorithms- event driven simulation, simulation events, compiled simulation; Structural vs. Behavioral modeling, Synthesis subset of HDLs, register transfer synthesis;	8
<b>Combinational Logic Design</b>	Logic design process; Combinational network delay, wire and gate delay, fanout, path delay; Power and Energy optimization, glitch analysis and optimization; arithmetic logic and FPGA implementation; physical design for FPGA.	6
<b>Sequential Machine Design</b>	Sequential machine design process, Top-Down Design, State Transition and Register transfer models, Moore and Melay FSM design and modeling ; Rules for clocking, Performance analysis of Flip-Flop based system, Performance analysis of Latch based system, Clock skew, retiming; Case study, Meta-stability, Synchronization;	8
<b>VHDL</b>	Different Descriptions and modeling styles, Simulations Cycles, Process, Loops, Delay Models, Library, Functions, Procedures, Synthesis, Test bench;	10
<b>Design using VHDL</b>	Multiplexer, De-multiplexer, Decoder, Encoder, n-bit adder, Multiplier, Sequence detector- Moore and Melay designs;	5
<b>Testing</b>	Fault models, Different faults, Fault simulation, ATPG, DFT, Boundary scan, BIST;	5
<b>Total No. of Hours</b>		<b>42</b>
<b>Textbooks</b>	1. Wayne Wolf, <i>FPGA- Based System Design</i> , Pearson	
<b>References</b>	1. Charles H Roth Jr, <i>Digital System Design using VHDL</i> , Thomson Learning. 2. Digital Design: Principles and Practices - John F Wakerly, Prentice Hall. 3. VHDL for Programmable logic, Kevin Skahil, AddisonWesley. 4. Digital systems Testing and Testable Design - Miron Abramovici, Jaico Publishing	

<b>SET/EC/BT/E709- INFORMATION THEORY AND CODING</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Module 1</b>	Entropy and Loss-less Source Coding : Entropy, Entropy of discrete random variables- Joint, conditional and relative entropy- Chain rule for entropy, Mutual information and conditional mutual information, Relative entropy and mutual Information; Lossless source coding- Discrete Memory-less sources, Uniquely decodable codes- Instantaneous codes- Kraft's inequality ó Average codeword length, Optimal codes- Huffman coding, Arithmetic Coding, Lemplel-Ziv Coding, Shannon's Source Coding Theorem.	10
<b>Module 2</b>	Channel Capacity and Coding Theorem: Channel Capacity- Discrete memory-less channels (DMC) and channel transition probabilities, Capacity computation for simple channels- Shannon's Channel Coding Theorem for DMC (proof is optional), Converse of Channel Coding Theorem Continuous Sources and Channels: Differential Entropy- Mutual information- Waveform channels- Gaussian channels- Shannon-Harley Theorem, Shannon limit, efficiency of digital modulation schemes-power limited and bandwidth limited systems.	11
<b>Module 3</b>	Channel Coding- Part-I: Introduction- Error detection and correction, Review of Vector Space, properties, Linear block codes- Construction and decoding, Standard Array decoding, Distance properties. Characteristics of Finite fields- Construction and basic properties of Finite Fields- Computations using Galois Field arithmetic- Extension Fields. Cyclic codes ó Non-systematic and systematic codes-Construction and Decoding- Minimal Polynomials, Conjugates and Conjugacy classes, BCH codes ó Construction and decoding - Reed Solomon codes, Introduction to low density parity check codes.	11
<b>Module 4</b>	Channel Coding- Part-II: Convolutional codes ó Encoder representations and Types- Maximum likelihood decoding - Viterbi decoding, Hard decision and Soft decision decoding, Transfer function of convolutional codes, Interleaving, Concatenated codes, Introduction to Turbo codes.	8
<b>Total No. of Hours</b>		<b>40</b>
<b>Textbooks</b>		
<b>References</b>	<ol style="list-style-type: none"> <li>1. Thomas M. Cover and Joy A. Thomas, "Elements of Information Theory", John Wiley &amp; Sons,</li> <li>2. Shu Lin and Daniel. J. Costello Jr., "Error Control Coding: Fundamentals and applications", 2nd Ed., Prentice Hall Inc, 2004.</li> <li>3. John G. Proakis and M. Salehi, "Digital Communication", 5th Ed., MGH, 2008</li> <li>4. David J. C. MacKay, "Information Theory, Inference and Learning Algorithms", Cambridge University Press, 2003</li> <li>5. Robert Gallager, "Information Theory and Reliable Communication", John Wiley &amp; Sons, 1968.</li> <li>6. R. E. Blahut, "Theory and Practice of Error Control Codes", Addison-Wesley, 1983.</li> </ol>	



<b>SET/EC/BT/C710-VLSI DESIGN LAB</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Digital VLSI Circuits</b>	<ol style="list-style-type: none"> <li>1. Learning tool flow of SYNOPSIS/ PYSIX/ CADENCE/Tanner tools (or any other CAD package for VLSI).</li> <li>2. Study of SPICE models for MOSFET.</li> <li>3. Creating and simulating basic CMOS gates.</li> <li>4. Creating layout of CMOS inverter; parasitic extraction; simulation.</li> <li>5. Simulating Inverter circuit for verifying effects of transistor sizing on delay and Power.</li> <li>6. Creating layout for NAND, NOR, AOI and Simulation.</li> <li>7. Modeling and simulating a wire.</li> <li>8. Fan Out 4 Delay.</li> <li>9. Designing and simulating a full adder circuit.</li> <li>10. Designing and simulating a latch/FF and SRAM cell.</li> </ol>	2x12
<b>Total No. of Hours</b>		<b>24</b>

<b>SET/EC/BT/C711-OPTICAL FIBER COMMUNICATION LAB</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
	Related Experiments.	2x12
<b>Total No. of Hours</b>		<b>24</b>

<b>SET/EC/BT/C712- PROJECT PREPARATION</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
	Project Preparation includes following assignments. <ol style="list-style-type: none"> <li>1. Survey and study of published literature on the assigned topic;</li> <li>2. Working out a preliminary approach to the Problem relating to the assigned topic;</li> <li>3. Conducting Preliminary Analysis/ Modelling/ Experiment/Simulation/ Experiment/ Design/ Feasibility</li> <li>4. Preparing a Written Report on the Study conducted for presentation to the Department;</li> <li>5. Final Seminar, as oral Presentation before a Departmental Committee.</li> </ol>	40
<b>Total No. of Hours</b>		<b>40</b>

<b>SET/EC/BT/C713 - INDUSTRIAL TRAINING SEMINAR*</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
	Student shall prepare a detailed report on her/his industrial training and deliver a seminar of 30 minutes.	2x14
<b>Total No. of Hours</b>		<b>28</b>

**SEMESTER VIII**

SN	Code	Course Title	L	T	P	T.A	C.T	TOT	ESE.	SUB. TOTAL	Credits
1	SET/EC/BT/C801	Wireless and Mobile Communication	3	1	-	10	20	30	70	100	3
2	SET/EC/BT/C802	Embedded Systems	3	1	-	10	20	30	70	100	3
3	*****	Elective III	3	1	-	10	20	30	70	100	3
4	*****	Elective IV	3	1	-	10	20	30	70	100	3
5	SET/EC/BT/C809	Advanced Communication Lab	-	-	2	30	-	30	70	100	1
6	SET/EC/BT/C810	CAD Lab	-	-	2	30	-	30	70	100	1
7	SET/EC/BT/C811	Major Project	-	-	12	30	-	30	70	100	7
<b>Total</b>			12	4	16	130	80	210	490	700	21

Elective III	S. No.	Code	Course Title
	1	SET/EC/BT/E803	Radar Guidance And Navigation
	2	SET/EC/BT/E804	IC Fabrication And Testing
	3	SET/IN/BT/E805	Renewable Energy Engineering

Elective IV	S. No.	Code	Course Title
	1	SET/EC/BT/E806	Satellite Communication
	2	SET/EC/BT/E807	Multimedia Systems And Communication
	3	SET/EC/BT/E808	CMOS Analog IC Design

SET/EC/BT/C801- WIRELESS AND MOBILE COMMUNICATION		
Module Name	Content	No. of Hrs.
<b>Module 1</b>	Introduction to RF propagation, multi-path fading, mobile channel description and analysis, RF circuits and systems	8
<b>Module 2</b>	Mobile communication concepts, cellular engineering, cellular concepts, frequency allocation, spectrum efficiency, speech coding, modulation/demodulation techniques, multiple access techniques-FDMA, TDMA, CDMA, Spread Spectrum Techniques.	12
<b>Module 3</b>	Error control coding for mobile channel, communication applications, capacity of cellular communication networks, mobile communication standards.	10
<b>Module 4</b>	Wireless data communication systems, wireless multimedia, ATM and IP, paging, wireless local loops. Mobile satellite communication, third generation cellular systems, GSM systems, universal mobile telecommunication systems.	14
<b>Total No. of Hours</b>		<b>44</b>
<b>Textbooks</b>	1. Rappaport, "Wireless Communication"	
<b>References</b>	1. William Stallings, "Wireless Communication and Networks" 2. D. R. KamiloFehar, "Wireless digital communication" 3. Haykin S & Moher M., "Modern wireless communication", Pearson.	

<b>SET/EC/BT/C802- EMBEDDED SYSTEMS</b>		
<b>Units</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Embedded Systems</b>	Definition, examples, design considerations and requirements; Embedded design life cycle. Product specifications, Hardware/Software partitioning, Iterations and Implementations, Hardware software integration, Product testing techniques, Hardware Software Co-design concept; System on Chip; Different software tools used for Embedded System design;	5
<b>Embedded Hardware</b>	Processor, Power supply, clock, memory interface, interrupt, I/O ports, Buffers, Programmable Devices, FPGA, CPLD, ASICs etc.; Interfacing with memory and I/O devices; Memory Technologies ó EPROM, Flash, OTP, SRAM, DRAM, SDRAM etc.; Bus architectures like I2C, SPI, AMBA, CAN etc.; Embedded processor selection and trade-offs. Hardware development cycles: Specifications, Component selection, Schematic Design, PCB layout, fabrication and assembly, testing ó functional, manufacturing, parametric;	6
<b>Microcontrollers</b>	Difference between microprocessor and microcontrollers; Special features of microcontrollers for control applications; Architecture of 8-bit microcontroller e.g. 8051, and its instruction set; Interrupt; Timer and Counter; serial communication with 8051; Interfacing microcontroller with memory, IO Devices, DC motor, Stepper motor; Features of advance microcontrollers e.g. WDT, PWM etc.	8
<b>High End Embedded Processors</b>	Introduction to ARM processor architecture and instruction set; Introduction to PowerPC processor architecture and instruction set;	7
<b>Embedded Software</b>	Concept of Firmware; Operating system basics; Device drivers; Real Time Operating System: Fundamentals. Multitasking application ó Threads: execution suspension, sharing, resources between tasks: timers, message queues. Concurrent programming concepts ó Tasks and Events: Synchronization and communication, task scheduling: Time slicing: priority: pre-emption scheduling interrupts and background tasks. Main features of QNX, Vx WORKS and LynxOS, Real Time Embedded System design and development;	12
<b>Design and Testing</b>	Embedded System Design: Embedded System product Development Life cycle (EDLC), Product enclosure Design and Development; Embedded System Development Environment ó IDE, Cross compilation, Simulators/Emulators, Hardware Debugging. Hardware testing methods like Boundary Scan, In Circuit Testing (ICT) etc.	6
<b>Total No. of Hours</b>		<b>44</b>
<b>TextBooks</b>	1. Vahid and Givargis, T., óEmbedded System Design: A Unified Hardware/ SoftwareIntroductionö, John Wiley and Sons 2. Noergaard, T., óEmbedded Systems Architecture: A Comprehensive Guide for Engineers and Programmersö, Elsevier Publications 3. Arnold S Berger, óEmbedded system design: An introduction to processors, Tools, Techniquesö, 4th edition, CMP Books, 1st Edition, 2001. 4. David Simon, An Embedded Software Primer, Addison Wesley, 2000. 5. Shibu K.V.: Introduction to Embedded Systems, Tata McGraw Hill, 2009	
<b>References</b>	1. Tim Wilmshurst, An introduction to the design of small-scale embedded systems, Palgrave, 2. J.W. Valvano, Embedded Microcomputer System: Real Time Interfacing, Brooks/Cole, 2000. 3. David Seal (Ed.), ARM Architecture Reference Manual, 2nd Edition, Addison-Wesley, 2001. 4. Steve Furber, ARM Sytem-on-Chip Architecture, 2nd Edition, Addison-Wesley, 2000. 5. Andrew N. Sloss, Dominic Symes, Chris Wright, ARM System Developer's Guide, Elsevier, 6. An Implementation guide to Real Time Programming - David L. Ripps, Yourdon Press, 1990. 7. S.Furber, ARM system Architecture, Addition wesley, 1996. 8. Raj Kamal, Embedded Systems. Architecture, Programming and Design. Tata McGraw Hill. 9. G.H. Miller, Microcomputer Engineering, 3d edition, Pearson Education. 10. Kang, C.M.K., and Shin, G., óReal Time Systemsö, McGraw Hill	

<b>SET/EC/BT/E803- RADAR GUIDANCE AND NAVIGATION</b>		
<b>Units</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>General Management</b>	Block diagram, range equation, performance factors, pulse and CW radar, moving target indicator, pulse, Doppler radar, delay line cancellers, tracking and scanning radar.	10
<b>Radar transmitter and receiver</b>	Different types of radar modulators, receivers block diagram and operations, low noise front ends, receiver protector, radar displays, A-scope and PPI, mixer, duplexer.	10
<b>Navigation Aids</b>	Radio direction finding, loop antenna goniometer, Adcock, error in direction finders, radar beacons, VHF and UHF radio range, LF/MF radio range, VOR, DME, hyperbolic navigation systems, loran-decca-tacan landing systems, GCAs, ILS, MLS, global positioning systems.	12
<b>Guidance</b>	Basic guidance, block diagram, internal guidance, Gyroscopes, Servo accelerators, basic application of server system components.	12
<b>Total No. of Hours</b>		<b>44</b>
<b>References</b>	1. Merrill I. Skolnik, "Introduction to Radar Systems" 2. N. S. Nagraja, "Elements of Electronic navigation" 3. R. S. Berkowiz, "Modern Radar"	

SET/EC/BT/E804-IC FABRICATION AND TESTING		
Module Name	Content	No. of Hrs.
<b>Introduction</b>	Overview of IC technology- CMOS, BIPOLAR, BI-CMOS, SOI; IC design flow; Basic fabrication steps and their significance;	2
<b>Crystal growth</b>	Crystal structure, lattice, basis, planes, directions, angle between different planes; Crystal growth; Crystal defects; Epitaxy, Clean room; conductivity, resistivity, sheet resistance;	4
<b>Oxidation and Film Deposition</b>	Wet and Dry oxidation; Setups for Oxidation; Various deposition techniques CVD, PVD, evaporation, sputtering, spin coating, LPCVD, Epitaxy, MBE, APCVD;	5
<b>Doping Methods</b>	Diffusion and Ion Implantation: Diffusion process, Solid state diffusion modeling, various doping techniques, Ion implantation, modeling of Ion implantation, statistics of ion implantation, damage annealing, thermal budget, rapid thermal annealing.	5
<b>Lithography and Etching</b>	Photolithography; Positive photo resist, negative photo resist, comparison of photo resists, components of a resist, light sources, exposure, Resolution, Depth of Focus, Numerical Aperture (NA), sensitivity, contrast, need for different light sources, masks, Contact, proximity and projection lithography, EUV lithography, X-ray lithography, e-beam lithography, ion lithography, SCALPEL; Wet etch, Dry etch, Plasma etching, RIE etching, etch selectivity/selective etch, etch directionality;	5
<b>Planarization and Metallization</b>	Planarization Techniques: Need for planarization, Chemical Mechanical Polishing; Metallization and Interconnects: Copper damascene process, Metal interconnects; Multi-level metallization schemes;	5
<b>Process Integration</b>	NMOS process, CMOS:N-well process, Twin well process, CMOS inverter fabrication and masks; Bipolar process; IC Manufacturing;	5
<b>IC Design</b>	Hierarchy, regularity, modularity, locality; Design methods: P/DSP, programmable logic, gate array design; cell-based design, full custom design; system on chip; VLSI CAD tools;	4
<b>Testing and Verification</b>	Testing and Verification: logic/ functional verification, manufacturing tests, testers and test programs, logic verification- test benches, regression testing; Manufacturing test principles: fault models, operability, observability, controllability, fault coverage, ATPG; DFT: ad hoc testing, scan design, BIST, IDDQ testing; DFM; Boundary Scan: TAP, TAP architecture, TAP controller, test instruction and data register;	5
<b>Packaging and other issues</b>	Packaging options, package parasitic, heat dissipation; Power distribution, IR drops, L di/dt noise, on chip bypass capacitance; ESD protection; Latch up and its prevention;	4
<b>Total No. of Hours</b>		<b>44</b>
<b>Textbooks</b>	<ol style="list-style-type: none"> <li>1. Gray S. may, Simon M. Sze, "Fundamentals of Semiconductor fabrication", Wiley</li> <li>2. James Plummer, M. Deal and P.Griffin, "Silicon VLSI Technology", Prentice Hall, 2010.</li> <li>3. Neil H. Weste ,David Harris, Ayan Banerjee, "CMOS VLSI Design, a circuits and systems perspective", Pearson.</li> </ol>	
<b>References</b>	<ol style="list-style-type: none"> <li>1. Jan M. Rabaey, A. Chandrakasan, and B. Nikolic, Digital Integrated Circuits: A design Perspective, Pearson Education, 2002</li> <li>2. S.M.Kang&amp; Y. Leblebici, CMOS Digital Integrated Circuits, McGraw Hill, 2002</li> </ol>	

<b>SET/IN/BT/E805. RENEWABLE ENERGY ENGINEERING</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Introduction</b>	Energy sources and their availability- conventional and renewable energy sources, prospects of renewable energy. Energy conservation and energy audit.	4
<b>Solar Energy</b>	Solar radiation and its measurement, solar constant, solar radiation at earth's surface, solar radiation geometry, estimation of average solar radiation, solar radiation at tilted surfaces. Photo-thermal conversion- Physical principles of solar radiation into heat, solar energy collectors- flat plate and focusing type, energy balance equation and collector efficiency, Selective absorbing coatings. Useful heat gained by collector fluid. Solar energy storage systems- solar ponds and extraction of thermal energy. Applications of photo-thermal energy- in agriculture, distillation, pumping cooking, green houses, hydrogen production, etc. Solar photo-voltaic: Principle and materials, solar cells, their combination, storage of photovoltaic energy.	8
<b>Wind Energy</b>	Nature of wind, power of wind, forces on rotor blades, wind energy conversion, energy estimation, site selection considerations, basic components of wind energy conversion system, types of wind machines- horizontal axial and vertical axial machines, aerodynamic forces acting on blades, scheme of electricity generation, generator control, load control, energy storage, applications of wind energy.	8
<b>Energy from Biomass</b>	Biomass conversion technologies- wet and dry processes, photosynthesis, biogas plants, fuel properties of biogas, thermal gasification of biomass.	4
<b>Geothermal energy</b>	Nature of geothermal fields, geothermal sources, energy estimation, application of geothermal energy, materials selection for geothermal power plants.	4
<b>Ocean energy</b>	Ocean thermal energy conversion (OTEC)- open cycle and close cycle OTEC, site selection, energy utilization, energy from tides, components of tidal power plants, Ocean wave energy- Energy conversion devices.	4
<b>Mini and micro hydro</b>	Components, turbine and generators for small scale hydro, protection, control and management of equipments.	4
<b>Chemical energy sources</b>	Fuel cells, design and principle, types, conversion efficiency, types of electrodes, work output and EMF of fuel cells. Batteries- basic theory, types, characteristics, different batteries arrangements. Hydrogen energy- methods of hydrogen production, hydrogen storage, hydrogen as an alternative fuel, safety and management.	6
<b>Total No. of Hours</b>		<b>42</b>
<b>Textbooks</b>	1. D. P. Kothari, Renewable Energy Resources, PHI Publications.	
<b>References</b>	1. G. D. Rai, Non- conventional sources of energy, Khanna Publishers, Delhi.	

SET/EC/BT/E806- SATELLITE COMMUNICATION		
Module Name	Content	No. of Hrs.
Module 1	Introduction: origin and brief history of satellite communication, elements of satellite communication link, current status of satellite communication.	5
Module 2	Orbital mechanism and launching of satellite: equation of orbital, locating the satellite in the orbit, orbital elements, elevation and azimuth calculation, geostationary, and other orbits, mechanics of launching satellite.	7
Module 3	Space craft: satellite subsystems, telemetry, tracking and command (TT and C), communication subsystem, transponders, spacecraft antennas.	7
Module 4	Satellite channel and link design: G/T ratio of earth stations, design of down links and uplinks, FM improvement factor	7
Module 5	Earth station technology: earth station design, earth station, tracking, low noise amplifiers.	7
Module 6	Multiple access techniques: frequency division multiple access (FDMA), FDM/FM/FMFDMA, time division multiple access, frame structure and synchronization, code division multiple access, random access.	9
<b>Total No. of Hours</b>		<b>42</b>
<b>Textbooks</b>	1. Pratt, "Satellite Communication"	

SET/EC/BT/E807- MULTIMEDIA SYSTEMS AND COMMUNICATION		
Module Name	Content	No. of Hrs.
Multimedia components	Introduction - Multimedia skills - Multimedia components and their characteristics - Text, sound, images, graphics, animation, video, hardware.	9
Audio and video compression	Audio compression - DPCM - Adaptive PCM - adaptive predictive coding - linear Predictive coding - code excited LPC - perpetual coding Video compression - principles - H.261 - H.263 - MPEG 1, 2, 4.	8
Text and image compression	Compression principles - source encoders and destination encoders - lossless and lossy compression - entropy encoding - source encoding - text compression - static Huffman coding - dynamic coding - arithmetic coding - Lempel ziv-welsh Compression, image compression	9
VoIP technology	Basics of IP transport, VoIP challenges, H.323/ SIP - Network Architecture, Protocols, Call establishment and release, VoIP and SS7, Quality of Service - CODEC Methods - VOIP applicability	8
Multimedia networking	Multimedia networking - Applications - streamed stored and audio - making the best Effort service - protocols for real time interactive Applications - distributing multimedia beyond best effort service - secluding and policing Mechanisms - integrated services, differentiated Services - RSVP.	8
<b>Total No. of Hours</b>		<b>42</b>
<b>Textbooks</b>	1. Fred Halshall, "Multimedia communication - applications, networks, protocols and standards", Pearson education, 2007 2. Tay Vaughan, "Multimedia: making it work", 7/e, TMH, 2007. 3. Kurose and W. Ross, "Computer Networking - a Top down approach, Pearson education, 3rd ed, 2005.	
<b>References</b>	1. Marcus gonalves "Voice over IP Networks", McGraw Hill, 2. KR. Rao, Z S Bojkovic, D A Milovanovic, "Multimedia Communication Systems: Techniques, Standards, and Networks", Pearson Education 2007 3. R. Steimnetz, K. Nahrstedt, "Multimedia Computing, Communications and Applications", Pearson Education, First ed, 1995. 4. Ranjan Parekh, "Principles of Multimedia", TMH, 2006	

<b>SET/EC/BT/E808- CMOS ANALOG IC DESIGN</b>		
<b>Units</b>	<b>Content</b>	<b>No. of Hrs.</b>
<b>Introduction and review</b>	Comparison of MOS and Bipolar Transistors, square-law ,regions ó cutoff , triode, saturation ,biasing , body effect, parasitic and equivalent circuits, short and long channel approximations, noise sources.	6
<b>Building blocks</b>	MOS amplifiers, source followers, cascades, differential stage design, folded cascade stages.	8
<b>Basic operational amplifiers</b>	Stability in opamps, systematic design operational amplifiers	6
<b>Multistage opamp design - I</b>	Fully differential amplifiers, current output amplifiers, rail to rail input and output amplifiers, Class AB and driver amplifiers.	7
<b>Multistage opamp design - II</b>	Feedback voltage and transconductance amplifiers, feedback transimpedance amplifiers and current amplifiers.	7
<b>Regulators</b>	Voltage and current reference circuits, terms - sensitivity Bandgap Reference: Principles, CMOS Bandgaps , Start-Up Circuits	8
<b>Total No. of Hours</b>		<b>42</b>
<b>TextBooks</b>	1. Wiley Sansen: Analog Design Essesntials, Springer 2006	
<b>References</b>	1. Philip E Allen, D R Holberg, óCMOS Analog IC Designö, Oxford University Press, 2004 2. BehzadRazavi óDesign of Analog CMOS Integrated Circuitsö, McGraw Hill, 2001 3. BehzadRazavi óFundamentals of Microelectronic Circuitsö, Wiley 4. Gray, May, óAnalysis and Design of Analog Integrated Circuitsö, Wiley.	



<b>SET/EC/BT/C809-ADVANCE COMMUNICATION LAB</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
	Related Experiments of Satellite Communication, Radar Guidance and Navigation, Microwave, Antenna, Wireless mobile Communication and Telecommunication Switching.	3x10
<b>Total No. of Hours</b>		<b>30</b>

<b>SET/EC/BT/C810-CAD LAB</b>		
<b>Module Name</b>	<b>Content</b>	<b>No. of Hrs.</b>
	Related Experiments of simulation and designing of Electronics circuits with Multisim, MatLab, LABView. Hands on Practice with NI RIO. DAQ and Elvis Board.	3x10
<b>Total No. of Hours</b>		<b>30</b>

<b>SET/EC/BT/C811- MAJOR PROJECT</b>	
<b>Module</b>	<b>Content</b>
	The Major Project will be evaluated on the basis of the weightage of 20% of Report writing, 50% of the Project work and 30% for Presentation and Viva. There shall be two presentations for each Project evaluation and at least one outside expert will be the member of the evaluation committee for final evaluation.